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Lucrative leasing model for enterprise customers of EV bike. and an innovative financing model for investors

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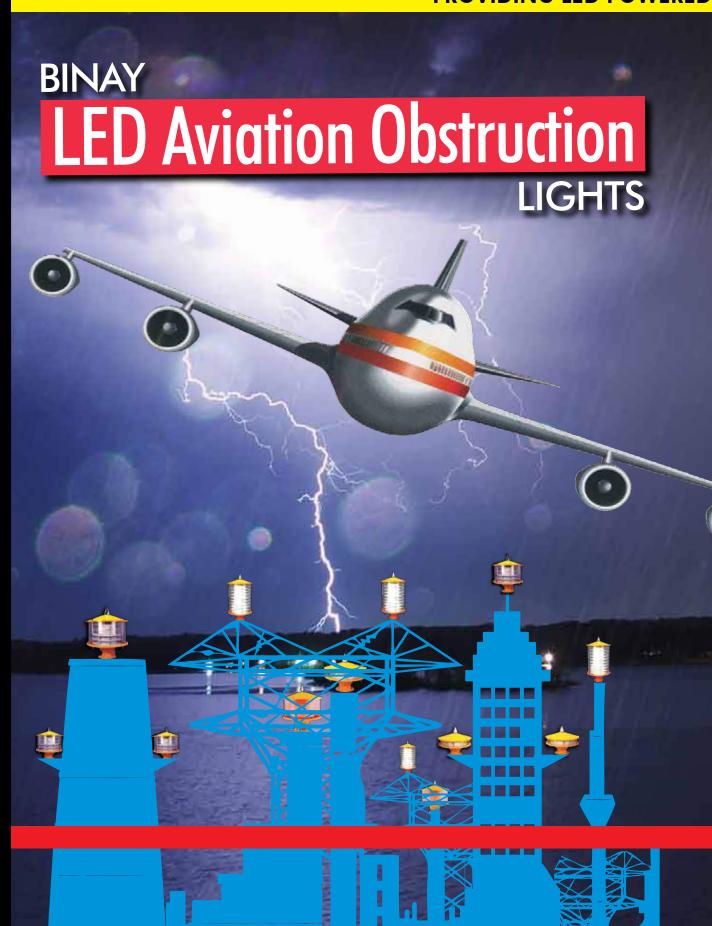




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BINAY LED-based HIGH, MEDIUM and LOW Intensity Aviation Obstruction Light Beacons

As per International Civil Aviation Organisation (ICAO) requirements
Available in Low Intensity, Medium Intensity and High Intensity versions
(as per International Civil Aviation Organization guidelines), BINAY's patented
LED Aviation Lights come with 5-year/3-year warranties



High Intensity 200,000 candela (Flashing White LEDs)



Medium Intensity 20,000 candela (Flashing White LEDs)



Medium Intensity 2,000 candela (Flashing Red OR Fixed Red LEDs)



All BINAY Aviation Lights are based on Non-switching passive type circuitry (NO SMPS)

The circuitry incorporated inside the LED Aviation Obstruction Lights is passive electronic (solid-state) in nature, and does not use internal SMPS driver switching units to control the light unit. Internal LED current control is by non-switching electronic means only. No electrolytic capacitors are used inside the Aviation Obstruction Light unit.

The above is necessary to ensure reliability of the LED Aviation Obstruction Light. This system design effectively eliminates any active components and circuits inside the aviation light itself, and enhances the solid-state passive reliability of the LEDs (mounted in relatively inaccessible locations at the height of tall structures). This drastically reduces the possibility of component failure - and hence possibility of any maintenance requirement - in the LED Aviation Obstruction Lights themselves (which are mounted at extreme heights in the system).

The BINAY LED Aviation Obstruction Light offers the following advantages:

- Fit-and-forget maintenance-free operation
- A long life of 100,000 hours (20 years at 12 hours daily burning)
- Pays for itself within a short period of operation in the form of reduced installation, maintenance and servicing costs
- · Quick Installation; Reliable operation 365 days per year
- Shock-proof and vibration-resistant
- Over-Designed Intensity to allow for natural LED intensity degradation over its operating lifetime

LED Obstruction Lighting for:

- Industrial chimneys and smokestacks
- Transmission, microwave and cellular towers

SCAN QR Code

- · Radio, TV and similar structural towers
- High-rise buildings and structures
- Airports and airfields

THE BINAY LED OBSTRUCTION LIGHT IS UNDER ACCEPTED PATENT, AND AS SUCH IS A PROPRIETARY PRODUCT



Binay Opto Electronics Pvt. Ltd.

(An ISO 9001:2015 Certified Company)

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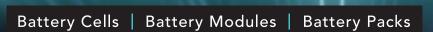


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CONTENTS

TECHNOLOGY

20 INTERVIEW: "Voltx.ai Is A Productivity Suite For Battery Designers...A Free Version Is Available" — Anton Doos, CEO, Voltx.ai

DESIGN

72 EMBEDDED: AI Hardware That's Tailored For Computer Vision

DO-IT-YOURSELF

- 78 CIRCUIT IDEA: Multi-User Call Bell For Up To Nine Persons
- 81 PROJECT: First Ever Finger-Size Full-Touch E-Ink Phone

STRATEGY

- **26** INNOVATION: Beyond The 4Ps Of Innovation
- 30 MANUFACTURING: Pick And Place Machine Selection Made Easy
- 40 AUTOMATION: India Semiconductor Mission: India Needs To Do Enough To Sit On The World Table



REGULARS

- 8 From You: Your Suggestions and Things You Wanted To Know!
- 10 Research & Innovation Updates
- 14 Truly Innovative Electronics
- 16 New Products

- 24 Dashboard: Key Industry Trends That Can Shape The Future Of Your Business
- 48 Startups & Innovators
- 56 Industry News: Industry Updates

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to Electronics For You (e-zine or print) to Access These Additional Stories...

TECHNOLOGY

19 FUTURE: Essentials Of A Battery Management System



27 TRENDS: NFTs -Not Frivolous Tech



35 TRENDS: Agricultural Robots: The Next-Gen **Farmers**



DESIGN

38 CIRCUIT: Imaging Sensor Technology And The Latest Developments

DO-IT-YOURSELF

- 62 PROJECT: Low-Cost Automation Using PIC16F676
- 67 PROJECT: Remote Controlled MP3 Player With FM Radio
- PROJECT: Smart SOS Device That Raises Alarm And Sends Location

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IoT PROJECTS

Please let me know some IoT projects published in EFY.

Manish Hosamani

EFY. Following are some IoT projects recently published in EFY:

- 1. Remotely Control TV With Alexa IoT TV Controller (December 2021)
- 2. Control Lights Through Thoughts Using EEG Based IoT Controller (November 2021)
- 3. IoT Based Automated Table Lamp (October 2021)
- 4. IoT On The Eyes: To Control Home Appliances (October 2021)

OPTICAL COMMUNICATION

I am interested in optical communication. Please let me know the links or EFY issues in which the topic was covered.

Subhash Arya

EFY. You may please go through the following articles available on our website:

- 1. An Overview Of Wireless Optical Communication: https://www. electronicsforu.com/market-verticals/ communication/wireless-optical-communication
- 2. Optical Networks Create Huge Demand: https://www.electronicsforu. com/tech-zone/test-measurement-elec-

tronics/optical-networks-create-hugedemand

UV STERILISATION BOX

This is regarding the 'Make Your Own UV Sterilisation Box' DIY article published in November 2021 issue. Where can I find the chokes required for this UV project? I couldn't find them online. Also, can you please share the video of the UV project?

Ammar

The author Ashok Baijal replies: I purchased the items online from Amazon, but these should be available in local electrical markets as well. They are commonly used in water filters. I used the Osram brand. Video of the project was not made, but photos are included in the article. It is simple to build. The chokes and tubes are held to the box using zip ties. I am using it without any problem till date for sanitising masks, keys, and phone after every use.

SEMICONDUCTOR MANUFACTURE

This refers to the article 'Industry 4.0 Can Boost Implementation Of Semiconductor Manufacturing' published in April issue. On the first page, EMS is wrongly mentioned as "environmental management services" while it actually stands for "electronics manufacturing services."

Sunil Banwari (The author)

EFY. Thanks for pointing out the mistake!

555 TIMER ASTABLE MULTIVIBRATOR

Where can I get the details of 555 timer based astable multivibrator using MATLAB?

Dhanush Shetty

EFY. Please see 'Demo of 555 timerbased astable multivibrator using MATLAB' DIY article available on our website at https://www.electronicsforu. com/electronics-projects/demo-555-timer-based-astable-multivibrator-usingmatlah

REPAIRING BOSE STEREO SYSTEM

I have been using a Bose stereo system for about 15 years but now it is not working. I sent it to a Bose service centre, but they could not repair it because some spare parts are not available. I had seen in good old days that local radio repair mechanics used to find the fault and repair within no time. Why it can't be done now? Please let me know if there is solution to this problem.

D.C.S. Gaitonde

EFY. Any audio system running for 15 years must be a good-quality product. However, as the technology evolved, the components used in audio systems and their technology have improved considerably. So, getting the old components now is very challenging. Even the authorised dealers may not have the old parts with them.

In this situation, first identify the exact part number of the faulty components and then check with some local radio mechanics. They may help you get the components either from their old stock or from some other sources. Once the parts are available, the local radio mechanic can fix your stereo, or you can get it done at an authorised service centre.

You can also search for other Bose servicing centres in India on the internet and check whether the parts are available with them.

Letters and questions for publication may be addressed to Editor, Electronics For You, D-87/1, Okhla Industrial Area, Phase 1, New Delhi 110020 (e-mail: **onedit@efy.in**)

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You may soon be able to stretch your TV



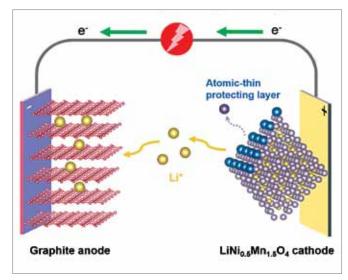
Stanford University researchers have developed a proof-of-concept of a reshapable display made up of a stretchy polymer called 'super yellow' and the synthetic plastic material polyurethane. It has a maximum brightness of at least two times that of a cellphone screen and can be stretched up to twice its original length without tearing. The display can be stuck to your skin, making wearable electronics a valuable market for this technology. "Imagine a display where you can both see and feel the three-dimensional object on the screen," says Stanford University chemical engineer Zhenan Bao.

Zhitao Zhang wears the flexible lightemitting film, featuring a Stanford logo, on the knuckle of their finger. This shows how the film can hold up to flexing and wrinkling (Credit: Zhitao Zhang and Jiancheng Lai of Bao Group Research Lab)

The lifespan of lithium-ion batteries doubled

Researchers at the University of Queensland have developed a technology that more than doubles the lifespan of highly sought-after high-voltage lithium-ion batteries, which achieve higher energy density but last only several hundred cycles. The researchers have demonstrated a battery that remains stable for more than 1000 charge/discharge cycles. They designed a uniquelygrown atomic-thin functional layer on the surface of a high-voltage cathode, which is the source of lithium ions and is a critical aspect that limits the cycle life in a battery. It should be noted that with the industry under increasing pressure to decarbonise, the development of lithium-ion batteries of lower cost, higher energy density, and longer cycle life is vitally important.

> Atomic-thin layer grown on cathode enables longlasting battery (Credit: https://www.uq.edu.au)

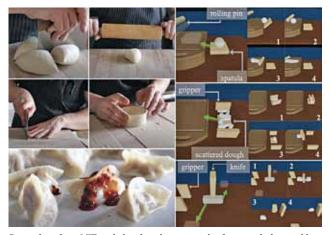


Honey based memristor for neuromorphic computing

Researchers at Washington State University have demonstrated prototype of a memristor made from an unusual material—honey! The memristor is the most crucial component in neuromorphic computing; it is literally a neuron realised on hardware. The researchers created this honey based memristor by turning real, bee-sourced honey into a solid form that was held between two metal electrodes. This design resembles synapses in the brain and how they are kept between pairs of neurons. The researchers anticipate that their findings lay the foundation for biodegradable, long-lasting, organic based computing systems that are much more efficient than traditional computing designs.

This ML framework helps robots make pizzas

Researchers at MIT, Carnegie Mellon University and the University of California, San Diego have developed a framework called DiffSkill for a robotic manipulation system that



Researchers from MIT and elsewhere have created a framework that could enable a robot to effectively complete complex manipulation tasks with deformable objects, like dough or cloth, that require many tools and take a long time to complete (Credit: https://news.mit.edu)

employs a two-stage learning process. This new machinelearning (ML) framework can enable robots to do sophisticated manipulation tasks using deformable objects, such as dough or cloth, over an extended period of time. Each step the robot must take to complete the objective is solved by a 'teacher' algorithm. Then it trains a 'student' ML model, which learns how to perform each skill required during an activity, such as rolling a pin. The researchers discovered that the student neural network could even surpass the teacher algorithm!

Artificial fingertips with e-neurons for soft robotics

Researchers at the University of Bristol have developed an artificial fingertip that emits signals that appear to be similar to those produced by the human touch nerve. The device is essentially a hollow elastomer dome with a thin



Robotic finger tip makes human-like 'nerve' signals (Credit: https://www.electronicsweekly.com)

wall that measures around 25mm wide. Short rigid rods in a contrasting colour are printed through the rubber. They found that their 3D-printed tactile fingertip can produce artificial nerve signals that look like recordings from real, tactile neurons. This is an exciting development in soft robotics—being able to 3D-print tactile skin could create robots that are more dexterous.

Manufacturing as an online service through 3D printers



Hui Wang (left), associate professor of industrial engineering and An-Tsun Wei, a PhD student, are the co-authors of a paper detailing how learning cloud data collected from interconnected 3D printers improves quality control and printing accuracy (Credit: FAMU-FSU Engineering)

FAMU-FSU College of Engineering researchers are working to improve 3D printing technology by teaching machines to learn from one another—a great example of group intelligence. The researchers used a cloud platform to connect different printers, and then had the machines communicate data on accurate processing, reducing the time it took to prepare and calibrate them. They also constructed a mathematical model to better understand the printing process. The technology demonstrates that data generated from multiple production machines can be shared with each in a timely manner, and manufacturing can be done as an online service for meeting diverse market demands.

Smartphone cameras may soon capture 3D images

Stanford University researchers have developed a novel method for ordinary image sensors to sense light in three dimensions using lidar. The research team's lidar system



The lab-based prototype lidar system that the research team built, which successfully captured megapixel-resolution depth maps using a commercially available digital camera (Credit: https://news.stanford.edu)

was able to capture megapixel-resolution depth maps with a commercially accessible digital camera. Simply put, their all-new approach is simple and integrates into everyday cameras like cellphones. With this technology, cameras will be able to estimate the distance between objects, resulting in a three-dimensional image that can be viewed on smartphones. This way, smartphone cameras may be able to capture three-dimensional images (3D).

New gate-oxide tech can produce better transistors

Engineers from the University of California, Berkeley have demonstrated a major breakthrough in the design of gate-

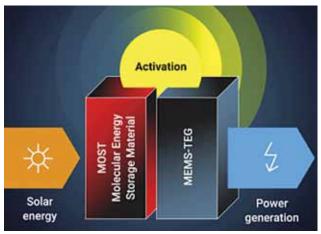
oxides of transistors that could significantly reduce their energy consumption without sacrificing speed, size, or performance. They did this by leveraging negative capacitance to make better transistors with the help of engineered crystal structures. The research shows how negative capacitance may be obtained in an engineered crystal made up of a layered stack of hafnium oxide and zirconium oxide that is compatible with modern silicon transistors. They were able to show that their gate-oxide technology is better than commercially available transistors.

Speech tech for information in seven northeastern languages

IIT Guwahati researchers are developing a speech technology tool that will enable retrieval of healthcare related information in seven northeastern languages. This new technology uses spoken keyword spotting (KWS) in northeastern languages. The project's KWS systems will be able to detect a list of predefined terms in a given voice signal in one of the project's target languages. Modeling speech with stateof-the-art deep neural network techniques will be part of the endeavour. People in far-flung parts of Northeast India should be able to obtain healthcare-related information in their own languages as a result of this project.

Electricity on demand from solar energy stored up to 18 years

Researchers at Chalmers University of Technology in Sweden have devised an energy system that can capture solar energy, store it for up to eighteen years, and then release it as needed. By connecting the system to a thermoelectric generator, scientists have now succeeded in making



The concept of MOST (Credit: https://www.sciencedaily.com)

it produce electricity. The new technique is based on the solar energy system called MOST—molecular solar thermal energy. Simply put, the technique works by using a specifically designed molecule that changes shape when exposed to sunlight. Then, a generator can be used to release the energy and transform it into power.

Probe to track soil moisture and density for agriculture

A research team at Cornell University has designed a new form of instrumentation tool called capacitance probe. It uses a number of sensors to track everything from solid



Jin Xu, one of the co-authors of the study, surveys sand dunes (Credit: https://www.azocleantech.com)

concentration to velocity and water content. All of this is done with unparalleled spatial resolution. The probes can reach deep and are small, hence collecting data on a millimetre scale to determine the right quantity of moisture and sand density. The investigation revealed how porous sand is, with a small quantity of air seeping through it. The scientists believe that their probe will be used for a variety of purposes, including studying how soils absorb and drain water in agriculture, and calibrating satellite measurements over deserts.

E-textiles could replace messy adhesive sensors for animals

Purdue University biomedical engineers and veterinarians have created a new remote horse slicker (a sort of coat) that can monitor a horse's cardiac, respiratory, and muscular conditions. It is made using e-textiles technology and uses Bluetooth for communication. They developed a dual regime spray to directly embed a pre-programmed pattern of functional nanomaterials into the slicker's fabrics. Adding e-textile qualities to clothes allows scientists, researchers, and clinicians to take advantage of the



The horse slicker is being tested on a horse running on a treadmill (Credit: Purdue University)

garments' already-existing ergonomic designs to achieve a commercial grade of wearability, comfort, air permeability, and machine washability.

A system that protects solar farms from cyberattacks

A new study from the University of Georgia recommends a novel strategy to protect solar farms, which might be a target of cyberattacks. A team from UGA's College of Engineering proposed a sensor system that monitors a crucial electrical component of solar farms for indicators of cyber-intrusion in real time. They devised a system that can identify anomalies in a power electronic converter's operations in real time, using only one voltage sensor and one current sensor. The system can discern normal conditions, open-circuit faults, short-circuit faults, and cyberattacks using deep learning approaches. Even if the firewall or security software fails to identify an attack, the sensors would detect unusual activity in the device's electrical current. The system can also perform diagnostic tests to discover the nature of the issue.

This 3D printing tech could help in cancer detection

Researchers at the USC's Viterbi School of Engineering have devised a highly specialised 3D printing process that permits microfluidic channels to be built on chips at a microscale previously unattainable. The researchers employed vat photopolymerisation, a sort of 3D printing process that uses light to manipulate the conversion of liquid resin material into its solid state. They were able to print even where the channel height was at 10 micron level and controlled it accurately to an error of plus or minus one micron. The new 3D printing platform's microscale channels enabled various applications, such as particle sorting. This could have a huge impact on cancer detection and research.



TRULY INNOVATIVE ELECTRONICS

INNOVATION UPDATES

Amongst numerous press releases of new products received by us, these are the ones we found worthy of the title Truly Innovative Electronics

Sensor for continuous measurement of liquids

VEGAPULS 6X from Vega is a universal sensor for continuous level measurement of liquids and bulk solids. The sensor is capable of measuring under all process conditions and

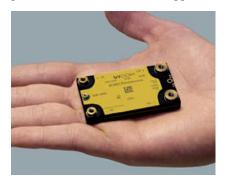


offers a self-diagnosis system. It is IEC 62443-4-2 compliant, which ensures secure communication and access control. The non-contact working of the sensor enhances the working life and ensures a maintenance-free operation.

Vega https://www.vega.com/en-us

High-density power module for automotive applications

Vicor's BCM6135 is a high-density power module for automotive applica-



tions. The 2.5kW module has a small form factor that can easily fit inside the palm of one's hand. The reduced size and weight of the power-dense module provides design flexibility, scalability, and space savings for all kinds of electric vehicle development. According to the company, this module can help reduce automotive power system size, weight, and number of components by up to 70%.

Vicor https://vicorpower.com

Industry's first Qi v1.3 certified wireless power receiver

Wireless Power Consortium (WPC) has certified Kinetic Technologies KTE7000 product as the first wireless



power receiver to achieve Qi version 1.3 status. The KTE7000 is a singlechip 5W wireless power receiver that conforms to WPC/Qi v1.3 Baseline Power Profile (BPP) standards. The Oi v1.3 specifications provide improved foreign object detection and enables device authentication. The Oi v1.3 can enhance automation of compliance testing, which reduces testing time and decreases potential interoperability problems.

Kinetic Technologies https://www.kinet-ic.com

World's first image compression DTV SoC

BXE-4-32 GPU from Imagination Technologies has been integrated into Realtek's RTD2885N to optimise image quality and significantly reduce system



bandwidth for an ultra-responsive user experience. Imagination's IMGIC technology available in BXE-4-32 provides lossless, frame buffer compression technology. The BXE enables higher resolution capability, lower silicon cost, and reduced bandwidth require-

Imagination Technologies https://www.imaginationtech.com

Single-antenna IoT connectivity solution

ELS62 from Thales' is a single-antenna solution that is compliant with



LTE-Cat.1bis standard. It provides a low-cost solution and simplifies design, thereby reducing development cost and time. The ELS62 module integrates security features to protect both the device and data, by ensuring robustness against cyberattacks, with strong device authentication and data encryption. It can be used in applications such as medical wearables, smart meters, and home alarm systems. Thales

https://www.thalesgroup.com/en

3-phase GDU for 48V automotive applications

ST Microelectronics L9908 is a 3-phase gate driver unit (GDU) developed for 48V automotive applications. The GDU



is capable of controlling 6 N-channel FETs for brushless motors. It has flexible I/O channels and three current sensors. The module is capable of operating in 12V, 24V, and 48V systems. It features a 32-bit SPI interface to offer diagnostic and protection features, such as real-time phase voltage monitoring along with open load detection and a few others.

ST Microelectronics https://www.st.com

Ultra-sensitive capacitive barometric pressure sensor

The BMP581 is a barometric sensor from Bosh Sensortec that detects fluctuations equivalent to one-thousandth of a mosquito's weight. The energyefficient sensor is a highly accurate altitude tracking IC that can provide precise location information for appli-

cations such as indoor localisation. floor detection, and navigation. The portable size and low power consump-



tion make it suitable for smart wearables, hearables, and IoT applications. Bosh Sensortec

https://www.bosch-sensortec.com

New-generation 3D touchless user interfaces

TouchNetix has developed fully integrated aXiom touchscreen chips that offer new 3D sensing capabilities by detecting air gestures, allowing



touchless functions in a variety of automotive, industrial, and consumer environments. TouchNetix' single-chip aXiom technology enables proximity sensing 10-15cm above the surface of the screen and provides much higher signal-to-noise ratio (SNR) than the traditional touchscreen controllers available in the market.

TouchNetix https://www.touchnetix.com

Energy harvesting and power management in same chip

Diatom NH16D3045 is a a highperformance energy harvester with an integrated power managment

solution for low-power applications. The IC can be used to charge a variety of energy storage elements, such as



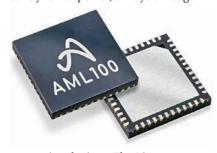
rechargeable batteries or supercapacitors, while it can harvest energy from light or from all types of solar PVs and from other energy sources, including kinetic, thermal, and radio frequencies. It is designed to work in the range of microwatts to a few milliwatts of power suitable for devices like smart wearables, electronic shelf labels, and wireless sensor networks.

https://www.nowi-energy.com

Diatom

World's first fully analogue machine learning chip

Aspinity's AML100 is the first product in Aspinity's AnalogMLfamily that can detect and classify sensor-driven events from raw, analogue sensor data. It allows developers to design significantly lower-power, always-on edge



processing devices. The IC consumes less than 20µA when in always-sensing, thus, enabling a ultra-low power always-on edge-processing solution for voice-first systems, acoustic event monitoring applications, predictive and preventative maintenance for industrial equipment, and biomedical monitoring.

Aspinity https://www.aspinity.com

COMPONENTS

Discrete GPU series

Intel Arc-A GPU series, which includes Intel Arc 3, 5, and 7, have been developed specifically for gamers and digital



creators. The Arc A-series is based on Intel's new Xe high-performance graphics micro-

architecture also called Xe HPG. Intel Arc 3 is a discrete graphics family for laptops, while the Intel Arc 5 and Intel Arc 7 are for desktops and workstations. The series supports DirectX 12 Ultimate and hardware-accelerated ray tracing and Xe Super Sampling (XeSS). The XeSS is Intel's AI based imaging technology that can enhance gaming performance.

Intel

https://www.intel.com

Hardware security modules

TrustFLEX ECC608 and Trust Anchor TA100 from Microchip Technology are Qi 1.3 compatible hardware security



modules (HSMs). The TrustFLEX ECC608 is an industrial grade

product with a pre-configured key for Qi 1.3 authentication while the TA100 is a crypto-automotive security IC developed for automotive security applications. Both the ICs provide support for secure boot and multiple key management protocols, such as transport layer security (TLS). The secure storage subsystem offers secure key provisioning, including X.509 certificates to cryptographically verify the source and quality of the certified power transmitter. Microchip Technology

Millimetre wave 5G chipset

Analog Devices' new millimetre wave (mmW) 5G front-end chipset is developed with the goal to reduce complex-



ity to produce faster radios. The chipset contains four

power-efficient ICs and works on a wide frequency of 24 to 47GHz radio waves. It features two single-channel (1T1R) up/down converters (UDCs) and two 16-channel dual-polarisation beamformer devices. The powerefficient ICs enable reduction of size, weight, power, and cost in mmW phased array designs.

Analog Devices

https://www.analog.com

Energy extracting PMICs

These ICs are optimised for intermittent and pulsed power inputs and are self-configurable with the ability to





automatically switch between buck.

boost, and buck-boost operations. The PMICs come with selectable and adjustable storage elements and protection mechanisms, which make them suitable for a wide range of applications, such as automation and IoT. e-Peas

https://e-peas.com

GaN converter

The 50W GaN converter from STMicroelectronics integrates a 650V GaN power transistor and has a very low power standby mode that consumes less than 30mW. It requires minimal additional components for designing



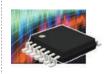
an SMPS, thus simplifying the design. The VIPer50GaN also offers good efficiency at all line and load conditions along with

multiple protection features. **STMicroelectronics**

www.st.com

Digital potentiometers

Omni Pro has released six digital potentiometers featuring direct interface



with incremental encoders and an adjustable debounce for mechanical encoder/switch.

These three terminal ICs have 64 viper control for adjustment of resistance and are suitable to replace mechanical potentiometers in high vibration, automotive applications. The ICs are available in 20k, 50k, and 100k versions.

Omni Pro

Omni Pro Electronics

HV cartridge fuse

Littlefuse has released the 828 Series of high voltage cartridge fuse that has



been designed and tested for use in compact automotive applications. The fuse has a

high interrupting rating and provides overcurrent protection. Its small form factor along with the high operating temperature range makes it suitable for applications as a protection device in the power distribution unit and onboard charger of electric vehicles. Littlefuse

https://www.littelfuse.com

Smart TV chips

Pentonic series of smart TV chips will enable TV manufacturers to offer

www.microchip.com



Dolby Vision gaming features. The chips can enable the Dolby Vision IQ with precision detail in the 8K and 4K smart TVs. These also allow users to watch multiple media sources in Dolby Vision in several windows at the same time. Besides, Pentonic chips improve latency through the auto low latency mode (ALLM) and variable refresh rate (VRR).

MediaTek

https://www.mediatek.com

BOARDS & MODULES

eFPGA module

Flex Logic Technologies' INFERX X1M provides great design flexibil-



ity and high performance at relatively low power

requirements, making it suitable for edge AI applications. The eFPGA module comes in a compact M.2 form factor and offers an edge logic accelerator. The module is suitable for edge AI applications, such as industrial machine vision, smart wearables, image processing, robotic vision, and analytics.

Flex Logic Technologies https://flex-logix.com

Embedded single board computer

Avalue Technology Inc. has released



an embedded single board computer, ECM-EHL, equipped with the Intel Pentium/Celeron/Atom

SoC BGA processor. Compared to its predecessors, it has improved performance, I/O capability, and better security features. It supports Linux as well as Windows 10. The module has extended temperature tolerance from -40°C to +85°C (with 0.5m/s air flow), which is suitable for industrial and outdoor applications under harsh operating conditions.

Avalue Technology Inc. https://www.avalue.com.tw

Linear power supply

Silent Angel has released the Forester F2, a linear power supply specifically made for the home Hi-Fi system. It offers four different linear power outputs



that are optimised for improving the musical performance in various

approaches. The power supply has toroidal transformers and low-noise MOSFETs. Its circuitry is designed to eliminate high-frequency electrical noises, make the transmission steadier, and reduce jitter and latency issues. The power supply is housed in an SECC galvanised steel box for better durability.

Silent Angel

https://www.silent-angel-audio.com

Al vision kit

qSmartAI80_CUQ610 from e-con Systems is a ready-to-deploy AI vision



kit. It features a Oualcomm OCS610 SoC based SoM, e-con's 4K MIPI low-light camera module, Sonv's

STARVIS 4K camera module, IMX415, and a carrier board. This ready-todeploy kit is designed for running image based machine learning (ML) and deep learning models on the edge. e-con Systems

https://www.e-consystems.com

1200W power supply

Powerbox OFI1200A, designed for conduction cooling applications in



industries, can operate between -40 and +95°C without a fan. The

1200W power supply is suitable for a wide input range between 85 and 305V AC with power factor correction. The module is available in three versions of single-output DC voltage: 12V/84A, 28V/43A, and 48V/25A. Each can provide 'near to zero' to the maximum rated output voltage and current. Powerbox Systems

https://www.powerbox-systems.com

T&M SOLUTIONS

Automated test solutions

The Rohde & Schwarz ZNrun K410 and K411 are capable of automatically analysing the Ethernet cable assembly



precisely. The ZNrun K410 software automates the time-intensive manual Ethernet

cable testing procedure following the IEEE 802.bj, by, and cd standards while the ZNrun K411 is capable of testing much faster IEEE 802.3ck Ethernet standard.

Rohde & Schwarz https://www.rohde-schwarz.com

Cable plus network software

Fluke Networks has upgraded its LinkIQ Cable + Network Tester soft-



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ware. The software is suitable for testing and troubleshooting IP net-



works and testing industrial Ethernet cabling. It provides reports including the expanded network testing and allows

users to document their work. The software now supports users in twelve languages and is available in all-new LinkIQ units, It is also available as a free upgrade to the existing customers. Fluke Networks

https://www.flukenetworks.com

Wi-Fi 6E testing solution

The Microwave Vision Group (MVG) and Anritsu Corporation have jointly developed a solution for testing the



new Wi-Fi 6 spectrum extension, Wi-Fi 6E. The new solution combines

MVG's multi-probe system and Anritsu's MT8862A wireless connectivity test set. The new product supports an effective, simple, and stable test environment for RF evaluation of the IEEE 802.11ax 6GHz band (Wi-Fi 6E) OTA measurements.

Microwave Vision Group: www.mvg-world. com, Anritsu Corporation: www.anritsu.com

SOFTWARE

EDA tool

The Pulsonix version 12.0 offers a 3D collision detection for both multiboard designs and board folding, and significant speed improvements over previous versions. The software adds around 100 new features compared to its previous version. Pulsonix V12.0 also has a dark mode, which reduces strain on the user's eyes and a simpler interface that reduces complexity for a new user.

Pulsonix https://pulsonix.com

NEW FEATURES

Power loss protection feature

Swissbit has launched a new power loss protection (PLP) feature called



'powersafe,' which is capable of protecting their industrial SSDs in the event of sudden power outages. During sudden power outages, extra power is pro-

vided to the SSDs to safely execute the write commands and flush data. This is achieved by using an additional PLP level to secure the data from the DRAM to NAND (cached data) or while the data is being transferred from host to controller (dynamic data). The tantalum capacitor used is one of the key components that makes the powersafe feature possible. Swissbit

https://www.swissbit.com

Improved response of power supply ICs

ROHM has unveiled QuiCur, a technology developed to improve the

response of power supply ICs that are used as DC/DC converter ICs



and LDOs. The response performance involves voltage stability and response

speed of subsequent stages. This technology is said to simplify the power supply circuit design by providing stable operation even with fewer external components, which will reduce the mounting area and thereby reduce the size of the product.

ROHM Semiconductor www.rohm.com

ENTERPRISE SOLUTIONS

Dome cameras for surveillance

The Project series dome cameras from Matrix Security Solutions feature 8Mp



back-illuminated CMOS sensor developed by Sony and a high MTF lens that enhances video quality in low

light conditions. These cameras can support SD cards up to 512Gb and have H.265 compression and motion based frame rate reduction system that reduces the memory requirement by up to 50%. The cameras offer a fixed and motorised varifocal lens that enables options to zoom in and out of the video footage and have an IP67 rating for dust and water resistance.

Matrix Security Solutions https://matsecurity.in

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"VOLTX.AI Is A Productivity Suite For BATTERY DESIGNERS... A Free Version Is Available"

EFY chanced upon Voltx.ai, a firm that claims to offer an innovative design tool for battery designers, and spoke to Anton Doos, the CEO, to understand the tool better and their revenue strategy



ANTON DOOS CEO, VOLTX.AI

Can you please explain your product in detail?

Voltx.ai is a productivity suite for battery designers. It has multiple tools along with a dashboard with a materials database where users can search, compare, edit, and add parts.

One of our tools, the Pack Designer, combines parts and generates over 1000 battery pack designs per second. Then it compares all the designs and shows you the best. Doing this manually would be impractical because it would take too long (in mathematics, it is known as a combinatorial explosion problem). So, by using automation tools, engineers can much more quickly gain insight and come up with the best designs.

Another tool, the Configuration Designer, is a digital grid paper for battery designers. We see many businesses relying on MS Paint or Excel to draw their configurations because there are no better alternatives. Our Configuration Designer lets engineers draw and flip cells and connections, change cell models, and in real time simulate shortcircuit, current load, and others. These automation features lead to faster and better designs.

How can users access Voltx.ai?

Voltx.ai is still in the alpha stage. A free version is available for users to use and submit feedback.

Any indication of the kind of fee that you are expecting to charge?

We are planning to charge US\$500 per month for five users. There is also an early adopters' discount for companies who provide us with feedback.

By when do you plan to turn this into a paid product or service? And are we correct to assume that it will be a software-as-a-service (SaaS) model?

Yes, it is a SaaS model with a freemium account. We

are on track to go live with paid accounts before summer (2022). Our intention for the free accounts is that DIY users can get sufficient utility and business users can try out the software before deciding to upgrade.

Since you may add more features to Voltx.ai later based on user feedback, won't that delay industry-readiness of your product?

I believe industry-readiness looks like a sine-wave adoption curve. Our modus operandi to advance adoption is to continuously identify user problems and add solutions as features. It is all under the umbrella of the overall vision of using automation to reduce the time between concept and manufacturing. When enough problems are solved, users will find enough value, and the product will be ready for market.

Through feedback from our early adopters, we believe our next set of features will make our software marketready. One example of this is, while talking to our users we identified that many wanted to export our designs to CAD to integrate into their existing workflow. Most businesses have standard operating procedures, and almost every engineering team relies on CAD at one point or another in their process.

Our solution created a new Export to FreeCAD button, where within twenty seconds of clicking the button the battery design will open as a CAD model on your computer and can be integrated into any existing workflow.

Why do you consider your product to be a milestone for battery design and production?

The algorithm we invented solves some difficult problems with very accurate results. We have compared the batteries designed manually by professionals with the batteries designed through our software. The results have shown very little difference. There is a considerable

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efficiency gain from this, going from tasks that take hours to tasks that take seconds.

Can you provide examples of some difficult problems solved by your algorithm?

One problem with battery design is that of a combinatorial explosion of possibilities due to the number of different cell models, configurations, and other parts.

Think of it like chess. The first chess computers quickly outperformed humans because they could analyse hundreds of thousands of moves quickly. The way people design battery packs now is like chess—humans use knowledge, experience, and intuition to constrain the total search size. This must lead to less than optimal results since the full search space of potential battery designs has not been created or analysed.

One of the ways we solved this problem is with a highly optimised algorithm that can generate the designs thousands of times per second. A second part is how we analyse and sort the results from most to least useful to present users with the optimal design.

What benefit can battery design engineers receive compared to any other similar product? What is your USP?

Other battery software we have seen focus on simulations, especially for EV packs. We focus on automating the design of any battery pack so that users can start manufacturing as soon as possible.

How is that different from other battery design tools?

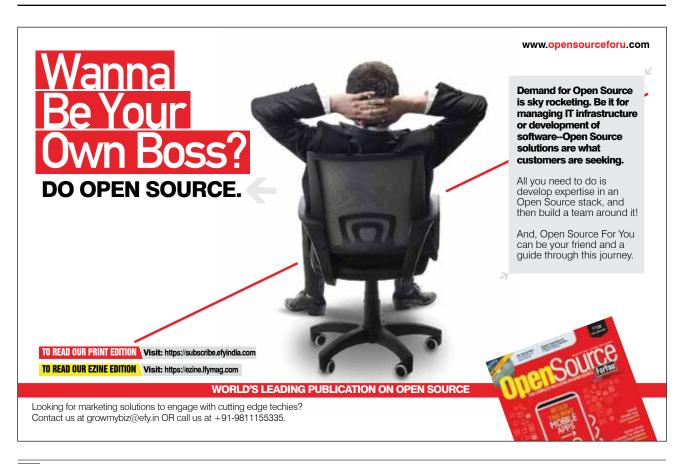
From what we have seen, other battery design software are usually a sub-feature of their main software. They have high-cost software for enterprises that are designing electric vehicles and large-format battery packs. These tools focus on simulations, particularly thermal simulations.

Think of a company like Toyota designing a new electric vehicle. Our software is for any business that designs battery packs, everyone ranging from DIY to startups to enterprises. It is lower-cost than competitors, and we are more focused on small and medium format packs, although we can produce large format packs as well. That means robots, drones, home power systems, or anything else you can imagine powering is what can benefit from our software.

Another big difference is our vision and overall focus on automation and Industry 4.0. The tools we provide are intended to speed up the engineering workflow—both to find the optimal design quickly and output the necessary information to begin manufacturing. What we are building today will run tomorrow's smart battery factories while our competitor products are not guided by this vision.

Which companies and institutions are using your software? What kind of products are they developing?

There is a wide range of people using the software. We have seen one company build a battery design for a giant solar yacht. We have seen electric bikes, robots, drones, home solar systems, and many other applications.







Semiconductor Shortage Likely to Ease Significantly in H2 2022

Global semiconductor chip shortages are likely to continue easing during the second half of 2022 as demand-supply gaps decrease across most components. Across PCs and laptops, the supply gap for the most important PC components like power management ICs, Wi-Fi and I/O interface IC have narrowed. Inventory levels of 5G-related chipsets including mainstream application processors, power amplifiers, and RF transceivers have increased significantly.

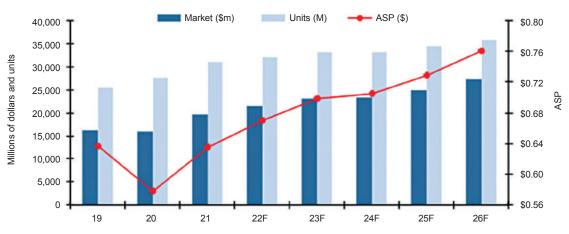
Component	Shortage Level in H1 2021	Shortage Level in H2 2021	Shortage Level in 2022	Price Increase 2022 (YoY)
AP/SoC - Flagship 5G	•	•	•	Up 5-10%
AP/SoC - Mainstream 5G	•			FLAT
AP/SoC - 4G	•	• •	• •	Up +10%
RFIC/FEM	•	• •		Up 5%
Memory - DRAM/MCP	•	•		Down 10-15%
Memory - Flash				Down 10%
CIS - 64MP and above			•	FLAT
CIS - 12 - 48MP	•			Up 0-5%
CIS - 2/5/8MP	• •	•		Down 0-5%
DDIC/TDDI	••••	• •	•	Up 5-10%
PMIC	• • •	• • •	• •	Up 15-20%
Packaging Substrate	•	• •		Down 0-5%
	Demand>Supply by 10% Demand>Supply by 10%-209 Demand>Supply by 20%-309	6 2. Improving shortage	with price competition in 202 ge to balance level in 2022 in cetain parts in 2022	22

- Demand>Supply by 30%-40%
- 4. Remains shortage in 2022

(Source: Counterpoint Research)

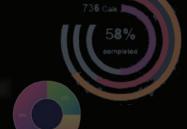
Microcontroller Sales Expected to Reach \$27 Billion by 2026

Microcontroller (MCU) sales boomeranged back with strong growth in the economic recovery during 2021, when the MCU market climbed 23% to a record-high \$19.6 billion. Between 2021 and 2026, total MCU sales are projected to increase by CAGR of 6.7% and reach \$27.2 billion in the final year of the forecast. In the next five years, sales of 32-bit MCUs are expected to grow by a CAGR of 9.4% to hit \$20.0 billion in 2026.



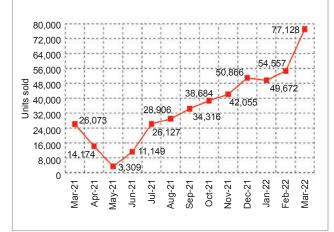
(Source: www.icinsights.com)

At-a-glance view of key industry trends that can shape the future of your business...



India's EV Sales Continue to Grow

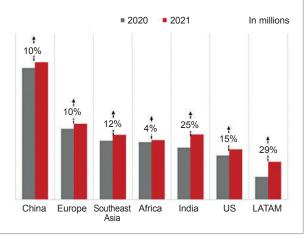
The overall EV sales in India for the month of March 2022 bounced back to its growth trajectory, clocking 77,128 units. This signifies a m-o-m rise of 41.4% and a y-o-y jump of 196%. EV registrations in March 2022 were driven by electric two-wheelers and passenger-type electric three-wheelers, which together accounted for 91.17% of total registrations in the month. The shares of these categories were followed by e-cars (4.62%), cargo-type electric three-wheelers (4.08%), and so on.



(Source: https://jmkresearch.com)

Refurbished Smartphone Market Shows Strong Growth In India In 2021

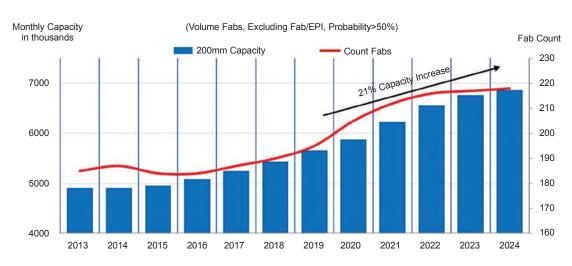
The global secondary smartphone market saw a surge in demand as well as supply in 2021. Even as new smartphone shipments grew 4.5% YoY in 2021, refurbished smartphone volumes witnessed a 15% YoY growth. Latin America and India lead with the highest growth rates, at 29% and 25% respectively. Apple remains the secondary smartphone market leader.



(Source: Counterpoint Research)

200mm Semiconductor Fab Capacity Set to Surge 21% to Mitigate Supply-Demand Imbalance

Semiconductor manufacturers worldwide are on track to boost 200mm fab capacity by 1.2 million wafers, or 21%, from the start of 2020 to the end of 2024, to hit a record high of 6.9 million wafers per month. After climbing to \$5.3 billion last year, 200mm fab equipment spending is expected to be \$4.9 billion in 2022 as 200mm fab utilisation remains at high levels and the global semiconductor industry works to overcome the chip shortage. Equipment investments are projected to remain above \$3 billion in 2023, with the foundry sector accounting for 54%, followed by discrete/power at 20%, and analogue at 19%.



(Source: www.semi.ora)



If you were offered a carbonated drink in a glass, what are the chances that you could easily identify whether it was Coca Cola or Pepsi? Would you even care? Well, hold on to that thought as we talk about several types of innovations first





eople have been talking about the 4Ps of innovation for quite some time. While these lenses have served well in the past, they are becoming outdated now. With the level of chaos that is going on around the world, I would say they are ripe for an update. But before we discuss the update, let us recap what those 4Ps are.

Product or service

The first P in the list of 4Ps is your product or service; the thing that you offer to your customers. So, when someone says innovate your product or service, they want you to create and introduce a new product or service to your customers. Sometimes, it does not have to be a completely new thing as an improved version of previous products or services qualifies. The caveat is, of course, there must be some enhancement in quality or overall performance. There must be

some material change.

This type of innovation is mostly driven by technological advancements, outdated product designs, changes in customer requirements, competition, etc. What is peculiar about this type of innovation is that it is clearly visible to customers. Mostly because they are the direct receivers of it.

Process

The second P in 4Ps is the process. It is all about how you create and deliver your product or service. This may include changes in the equipment and technology used in making the product. In addition, there might be an improvement in tools, techniques, and solutions used to help in making and delivering the product or service. It also includes processes you use to maintain your goods, accounting methods, customer service, etc.



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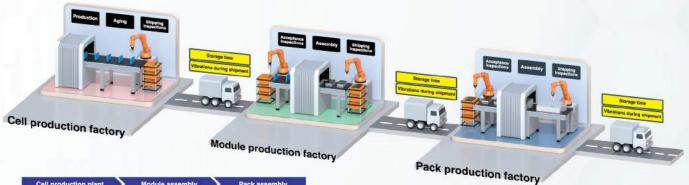
Quality Testing Maintenance Inspections R & D

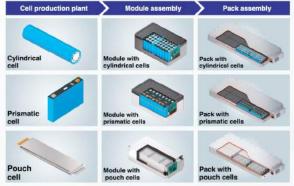
HIOKI battery testers are ideal for use in testing, development and inspections





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"We want to manufacture batteries with stable performance."

"We want to manufacture highly safe batteries."

This change may not always be visible to customers, and they may not even realise it sometimes. But from a business's point of view, process innovation often leads to bottom-line impact.

Position

Third P is about product positioning in the market. It is about who do you offer your product or service to. It is about the story you tell about your product or brand. This helps create a unique position in customers' minds and the market.

Most of the time, positioningrelated innovation is geography or demography-specific. Customers do not easily recognise this innovation as it may not happen directly. However, if the existing customers are part of the same geography or demographics where you reposition your product or service, they can easily recognise it. The outcomes usually affect the business's topline, expanding the market, and hence the sales base.

Paradigm

The last one in 4Ps is paradigm innovation. It is quite complicated compared to the other three. And that is because this innovation is about the business's mental model. that is, how your business thinks about why you do what you do and for whom. You can also call it a high-level business model.

It is the hardest to handle because it expresses your understanding, which you want your customers to appreciate. Nonetheless, many companies use it during their brand revival efforts.

Now back to my main argument regarding the significant update to this 4P based thinking. A lot has changed in the last few years, especially in the last two years. And as the change percolates through various aspects of society, it will affect businesses and how they approach innovation in their business.

We often insist on data to make informed decisions. But this is a data abundance economy. Everyone has enough data to prove their point.

It is not about right or wrong data. Every data has some perspective. This puts every business in a strange fix. It compels us to look beyond data, which can be more powerful and useful.

consumed, drinkable water wasted, etc. And that means, if they were to pay for all the environmental costs, according to Harvard, their real profits would be about -11.6%, a loss!

What about Pepsi? Pepsi's net revenue during 2018 was about \$64.7 billion, about twice as much. Pepsi's environmental impact cost was about \$1.8 billion to generate this revenue. And it means, if Pepsi

SO, THE PRODUCT, PROCESS, PARADIGM, OR POSITIONING INNOVATIONS ARE NOT HELPFUL. THIS IS THE NEW WORLD. THE 4Ps OF INNOVATION HAVE SERVED WELL IN THE PAST. BUT NOW, THEY ALONE ARE NOT ENOUGH. WE NEED TO UPDATE OUR PERSPECTIVE AND THINK BEYOND THE 4Ps OF INNOVATION.

How can it be done? Let me show you an example to prove my

At the beginning of this article, I asked you to think about carbonated drinks and your choice. People were clearly divided between Coca Cola and Pepsi until the last few years. They still are. Both brands have tried their hands at all the 4Ps of innovation successfully. But now, if you combine their 4Ps with available data and look at the bigger picture through a different lens, it will change your perspective significantly.

Allow me to share some data. And this data is coming from a Harvard study. It is called impact weighted accounts initiatives (IWAI). Using sample data and findings from that study, here is how these two brands stack up.

Coca Cola's net revenue during 2018 was approximately \$31.8 billion. But to generate this revenue, its total environmental impact cost was about \$3.7 billion. We are talking about many things, such as plastic waste produced, drinkable water

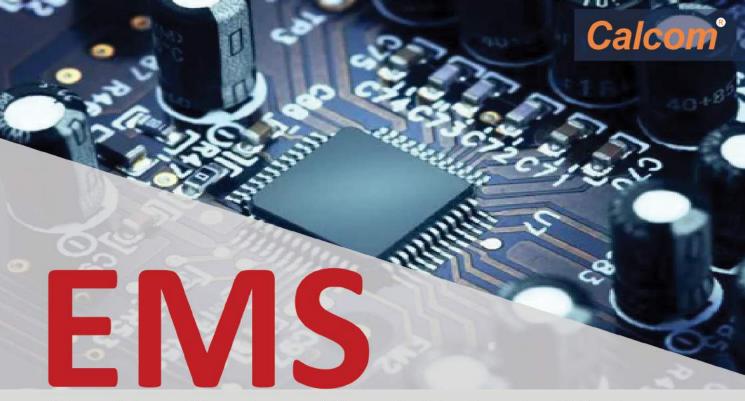
were to pay for all the environmental costs, their profits would be down to about -3%, also a loss. And remember, we are talking about a like to like comparison here.

The bottom line is, Coca Cola is four times 'dirtier' than Pepsi. Does that change your preferences? What do you think about it now? Do you see a point favouring one over the other?

I do not see why I would choose one over the other. For me, both do not fit my personal brand promise. Both are not aligned with what I am about as a person. And it is not just about these two brands of carbonated drinks, but also about all the others. They all affect the ecosystem similarly, some more, some less.

So, the product, process, paradigm, or positioning innovations are not helpful. This is the new world. The 4Ps of innovation have served well in the past. But now, they alone are not enough. We need to update our perspective and think beyond the 4Ps of innovation.

What do you think? **EFY**



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PICK AND PLACE MACHINE Selection Made Easy

If you are planning to purchase a new pick and place machine for your manufacturing unit, here are a few insights from experts that could help you select the best machine for your specific requirement

ABBINAYA KUZHANTHAIVEL

pick and place (PnP) machine makes use of comprehensive mechanisms to mount small electronic components and SMDs, such as capacitors, resistors, and ICs, onto a printed circuit board (PCB). It ensures that the small components are picked up carefully and placed accurately onto the PCB. The machine can be manual requiring placement by hand, semi-automatic with software assistance for placing, or automatic for typical mass manufacturing units with in-line facilities. Combined with their respective strengths, here are a few parameters to consider before investing in the machine.

The PnP machines have considerably improved recently to achieve speed with accuracy, and flexibility. However, standard requirements for specific parameters, like accuracy rate, component sizes, and board sizes, have not changed much. The future of



Yamaha Motor's Z:TA-R YSM40R (Credit: https:// global.yamaha-motor.com) SMT production is anything but predictable according to experts. With rapidly changing build schedules, there is a proliferation of new materials and advanced components to locate, load, mount, and trace. As these high-mix challenges go mainstream and mid-volume, real productivity and throughput suffer. Several equipment manufacturing firms are trying to solve precisely these challenges.

Let us have a look at some of the features and the challenges so that you can select the most appropriate machine for your production house.

Machine construction. When selecting a PnP machine, the most important factor is its construction. It determines the machine's effective components per hour (CPH) number, footprint, and the number of component feeders it can accommodate. The construction may either be all-welded steel or a bolted-together frame.

The most accurate machine may come with a welded structure to provide significant stability for accurate positioning and high-speed movement in X and Y axes. This suits any production environment without need for calibration. Apart from this, careful selection of centering methods and component positioning systems can support optimisation.

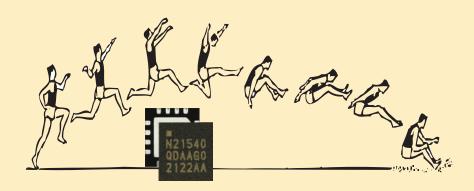
Placement speed. CPH is the measure at which the machine will pick up and place components onto the PCB. The speed measured in CPH will have a direct impact on production volume. Many PnP machine manufacturers rate their machines based on the IPC 9850 standard, a general guide for part mix and PCB placement arrays of their machines. It helps determine the throughput after accounting for feeder changes, transfer





Nordic nRF21540

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nRF21540 SiP includes below features:

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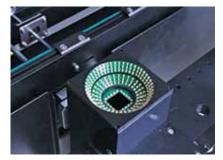
Juki Corp's RX-8 (Credit: https://www.juki.co.jp)

time, and other adjustments along the entire assembly process.

The industry is working towards faster machines every year to match the increased speed demand. Yamaha Motor's ultra-high-speed modular Z:TA-R YSM40R is one of the recent launches claiming to offer 200,000 CPH. The speed is said to be achieved by edge technology, including innovative high-speed rotary heads and linear motors incorporating new, high-speed algorithms, with the machine width of just one metre.

Juki Corp's RX-8 is another recent launch that claims to have the highest placement rate per square metre with CPH up to 100,000. It is compact and modular, featuring 998mm width. The new P20 placement head, designed for placing ultra-small chips and small integrated circuits (ICs), is credited for high-speed picking and placing from the single reel.

Intelligent camera systems. A camera system with high resolution improves the overall speed of the machine. New machines focus on their camera systems, like the one in the Neoden 8 PnP machine. It comes with the HR Flyvision camera



Vision system inside Neoden 8 PnP machine (Credit: https://neodenusa.com)

system for component recognition and Double Mark cameras to reach the extreme end feeders for better calibration.

Component handling. The PnP machines handle the minimum and maximum component dimensions according to the production needs for chip-scale package (CSP), ball-grid array (BGA), and other odd parts. The machines need various alignment methods to handle such a range of components, say, from minimum sizes of 0402, 0201, or 01005. Tiny components smaller than 0402 may require additional nozzles. The machines with single alignment give more precision and speed. An alternative alignment system can handle large component sizes.

However, one should remember, not all machines that can handle large components can also inspect them. Hence, most of the new launches with the latest vision system and software can take on

everything from 01005-size flip chips and CSPs to large quad flat packages (QFPs) and ball grid arrays (BGAs). Even tall, odd-shaped and throughhole components can be mounted with ease.

For instance, ASM Assembly Systems has a complete solution from component pickup, image recognition to placement of odd-shaped components. Its Siplace TX models are said to work with up to 48,000 CPH with minimum 0201 (metric) and maximum component size of 8.2mm \times 8.2mm \times 4mm.

Number of pick up heads. A PnP machine has single or multiheads to grab different shapes and heavy parts. Equipped with a number of nozzles to grasp the components, the head is movable in different directions simultaneously to ensure high productivity and flexibility.

For instance, the Yamaha YRM20 comes with a rotary RM head that is claimed to achieve 115,000 CPH. It features a one-head solution that can carry components of odd shapes and eliminates the need for head replacements, thus improving the component feed speed.

Similarly, the Neoden 8 model has eight synchronised nozzle heads to give repeatable placement accuracy.

Smart feeders. Recent machines focus also on the feeder capacity.

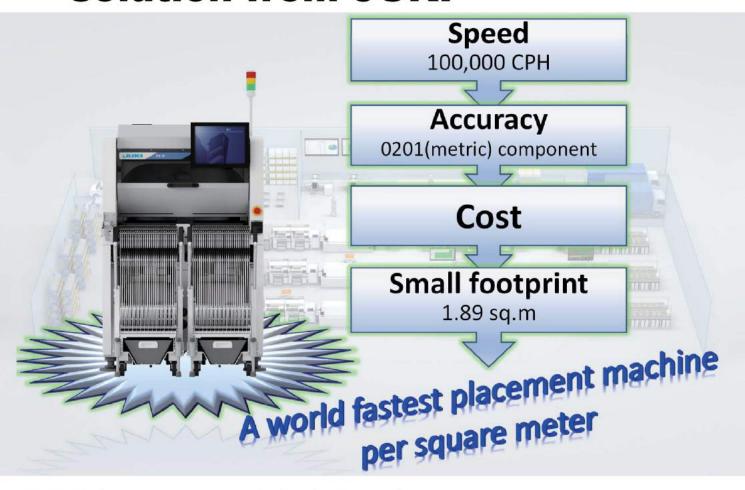


ASM Assembly Systems machine (Credit: www.asm-smt-tools.com)



RX-8

A new high speed placement solution from JUKI



JUKI Smart Factory Solution Products



Storage system ISM3600

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Reflow Hoven 3D AOI RV-2-3DH Insertion JM-50

RP-2B

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Yamaha YRM20 (Credit: https://global.yamaha-motor.com)

The larger components may require larger feeders. So, it is advisable to figure out the number of slots needed for larger tape feeder types, say, from 4mm to 56mm.

Mycronic MY300 has an 'intelligent feeder' solution that is claimed to take less than ten seconds of loading time. Agilis tape and stick feeders and tray handling systems in the machine are said to handle a wide range of components to meet the needs of today's SMT manufacturing grounds. The compact 4, 8, 12, and 16mm Agilis tape feeders come with an innovative cover-tape separator on the pen-sized feeder that exposes components for easy picking. The feeders provide quantity and location tracking, operator guidance, simplified data entry, moisture sensitive device (MSD) handling, and on-the-fly changeovers. Fully operator-independent, it ensures higher productivity in a smaller footprint. Its product set-up time is another important feature for quick processing.

With the SMT measuring feeder feature in place, there is no need to measure materials manually, which improves the efficiency and avoids

the occurrence of material error. The recent machines feature component verification before placement to ensure the right component is placed at the right location and comes with RCDT (resistor, capacitor, diode, and transistor) measurement tools. Neoden 8 features pneumatic feeders.

Component lead pitch. Industry experts consider 0.012 fine pitch as an unsaid thumb rule for selecting PnP machines. The component lead pitch is the distance

between the centres of the leads. However, experts warn buyers to not be moved by motor specifications or accuracy as even machines with great motor accuracy may not pick up, inspect, and place fine-pitch components.

Smart bin system. The most common reason for unplanned downtime in high-mix electronics manufacturing is missing material. A smart bin system helps machine operators minimise downtime by ensuring that all needed material is collected and prepared just-intime for production. Building on other powerful material handling solutions, the system brings easeof-use to the kitting and changeover process. The end result is faster changeovers and higher utilisation.

Next-generation precision. The latest pick-and-place solutions combine rigid machine frame, advanced mount head technology, and automatic thermal adaption for the highest levels of precision. The MY300 adds an improved line scan vision system to take this accuracy into the next generation. It combines three programmable light sources with either 2K or 4K resolution to ensure a future-proof solution for the most advanced components down to 0.15mm pitch. Both options provide extremely precise on-the-fly positioning and inspection for any type of miniaturised or specialised component

Accuracy and repeatability.

Experts' candid recommendation regarding accuracy for PnP machines is 0.00254mm (0.0001-inch) with a fine pitch lead of 12mil repeatedly. Though such high accuracy is commonly characterised for high-budget machines, one can always find a used PnP machine to attain such results. Besides, remember to buy a machine that comes with software suites to handle the process.

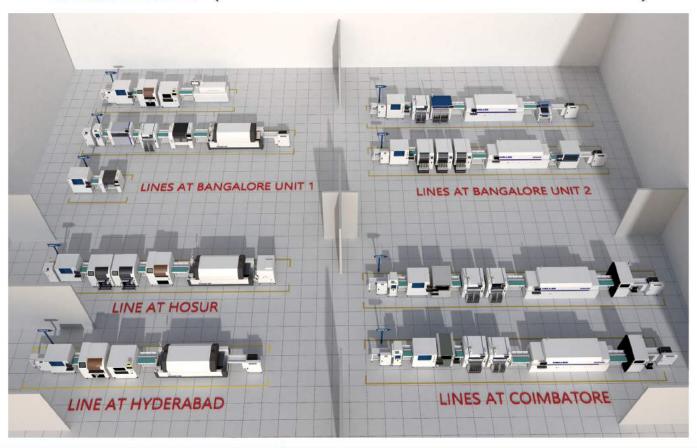
Featuring a single turret head



Eight synchronised nozzle heads of Neoden 8



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with eight or twelve nozzles, Europlacer's ii A1 model is said to deliver flexibility in handling a broad range of components with 164 feeder positions and two internal matrix trays, all in a compact footprint. It also features the firm's patented intelligent conveyor with board stops under full software control.

Software suite. If you are considering purchasing automatic machines, how are they programmed and what are the software used should be answered. The software has three primary goals of easy use, flexibility, and optimising performance.

Whatever, remember to check if your model comes with a computer or just the software. This can neither be good nor bad as some users prefer to install the software on their own PCs. A fully integrated system is the best choice to avoid software compatibility issues, however.

Also check if the machine runs on a familiar graphical user interface (GUI), such as Windows, or a proprietary system. Most operators prefer a standard GUI since a proprietary GUI may require a longer learning curve. CAD download, teaching camera, barcode readers, management information system (MIS), and optimisation functions and off-line program-



Europlacer's ii A1 model (Credit: https://europlacer.com)

ming can all make the operator's job easy and efficient. Hence, a strong software suite is important to make the process easy and traceable.

For instance, DDM Novastar adds a unique programming software—SMTrue Run Optimise—to the full-scale SMT assembly services

it offers. The software is used for making offline programming of automatic SMT machines to derive increased efficiency and throughput.

ASM Assembly System also has IIoT interfaces for integration into line and factory systems (ASM OIB, IPC-Hermes 9852, IPC-CFX) and the cloud (ADAMOS) with advanced workflow solutions for setup processes, material management, factory monitoring, and factory integration.

The new machines are also using a lot of open source software. OpenPnP is an open source SMT pick and place system that includes ready-to-run software and hardware designs that you can build and

> modify. Index PnP is an open source pickand-place machine that is among the recent launches designed for mass-production volumes typical of crowdfunding projects.

Other important considerations

Populating more boards and machines that are fit for any intelligent factory, complete with fully automated material flow, faster changeovers and open interface software



Mycronic MY300 (Credit: www.mycronic.com/en)

is the real pursuit of the industry. There is no limit to the minutiae of pick-and-place machine specifications. You may have to select the features as per your needs.

Unlike the rapid placers, precision placers often do not use mounted nozzles. They may rely on gantry-supported moving heads with relatively few pickup nozzles. While the conventional four-head vertical heads were only available in table-top machines, now they have evolved into automated machines.

Some machines may have to optionally be fitted with a dispense head for depositing adhesive on the board. If you require this feature, consider the dispense method, dot size, and dispense speed.

Maximum and minimum PCB or panel size and thickness can be critical requirements for some manufacturers. Fiducial recognition. coordinate correction, and bad mark detection should be standard for any automatic PnP machine.

New machines have a 3D stereo measurement feature for the through-hole device that easily identifies the image of the vertical pin and height of the component lead to detect bending deformation. This tells whether the through-hole components are placed accurately to ensure the correct height after placement, and thus reduces the need for operator's visual inspection. Simi-

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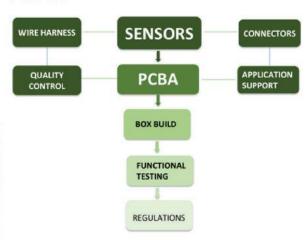
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larly, pattern feature recognition is another aspect intended for special components to recognise irregular placements and ensure accuracy.

Experts also try to find the best acceleration. In many cases, the acceleration of large components is set manually. Recent upgraded machines can automatically detect and accelerate to ensure the quality of placement and maximise the utilisation of equipment.

The next idea catching up is of PCB placement inspection. Before valuable components, such as BGAs and land-grid array (LGAs), are placed, interfering object detection is carried out in the placement area below these components. The foreign material/parts in excess are found in time to avoid placement defects. This can check whether there are any missing components under the shields before they are placed, which may play a good protective role in improving product quality.

The automotive electronics industry is expected to increase the demand for SMT continuously. Considering that automotive products have higher requirements for personal safety, service life, and outdoor use conditions, the reliability and durability needs of automotive electronic products are higher than those of others.

New PnP machines feature technologies like auto-cut sensor and advanced sensor technologies. Auto-cut sensor technology is a human safety feature that temporarily stops the process to avoid serious hazards or machine damage on human intervention. Advanced sensor technology helps a specific set of

The article has been compiled with key inputs from:

- · Jae Won, ASMPT Solutions
- · Priyesh S, Chipmax Designs Private Ltd
- · Mohamed Tharik Ajee, Enthutech
- Arvind Gopal Joshi, Accurex Solutions
- Suresh Nair, Leaptech Corporation



sensors, like in-sensor, out-sensor, and waiting sensor, in tracking the PCB at every stage in the production process.

Auto-optimisation is another feature to consider as it can suggest feeder and nozzle measurements. There is also a feature called regular automatic detection for nozzles. The closed-loop control of the nozzle ensures that the nozzles on the placement head are always in a normal clean condition, and the contaminated and damaged nozzles are automatically replaced in time.

Choosing the best

The latest generation of the placement machines sets new records in speed, floorspace performance, and accuracy for high-volume production applications. Smart factory solutions are fast becoming a trend in the electronics manufacturing industry to cut down on costs and improve efficiency of production.

However, buyers should pay attention to performance and usability in relevance to their manufacturing needs. Machines are available in various price ranges with a wide range of customer support and

maintenance. Keeping in mind the investment on machine is vital to avoid spending a fortune on costly outsourced machines.

While the classic PnP vision systems are difficult to work in areas requiring production tolerances and deviations, Schubert Group-a company developing modular packaging machines—suggests its neuronal networks based on artificial intelligence could be a solution of the future. "The image processing system therefore learns on its own and sorts the products into classes independently in a longer learning process. This means that the parameters are selected by the system and no longer by the programmer. For customers, this means a significant step towards independence—In the future, they could actually use neuronal networks, under certain conditions, to teach their packaging machine to handle a new product on its own," writes Dr Abdelmalek Nasraoui on the company website. **EFY**

Abbinaya Kuzhanthaivel is a technophile at EFY with interest to write on electronics, communication networks and open source



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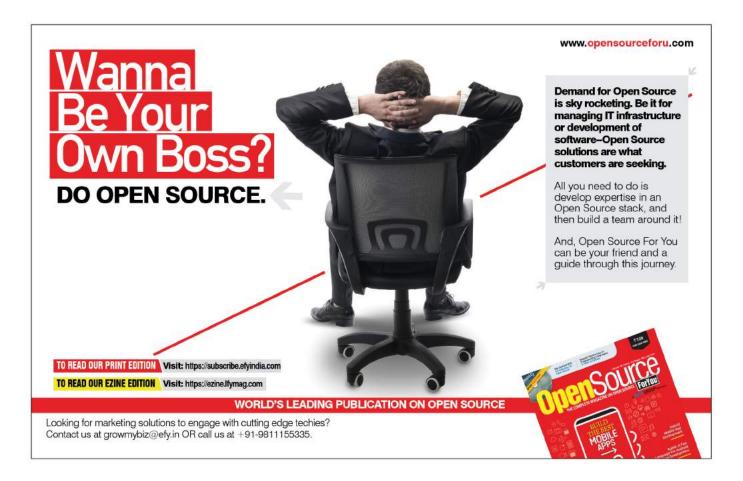
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The government's strong foot forward with regards to the semiconductor space was a much-needed move and, although late, it is expected to herald the golden era of the electronics industry of the country

SIDDHA DHAR

ven kids today are aware that there are things called semiconductors and chips. Some of them even know where these are used thanks to the semiconductor shortage that has been rattling the world since the onset of the Covid-19 pandemic.

But in India, this awareness is not just an outcome of the global chip crisis. The government of India's recent endeavors to make the country self-reliant in the areas of chip manufacturing has a greater part to play in beginning a conversation around hardware manufacturing in every other household, something very few in the country cared about before.

The government has come up with an ambitious ₹760 billion incentive to build the semiconductor industry of the country. Although the roots of it lie in reducing its dependence on China, the move has been long awaited by the industry, and thus, highly lauded. Better late than never, right? However, no matter how ambitious the plan, the

industry which has been neglected for the longest time now needs to buckle up and get a few things straightened out before it can dive headlong into the game.

"I think we always talk about three aspects: economic security, cybersecurity, and infrastructure security. The infrastructure in particular needs to be built. It is high time because otherwise we will be in trouble in the future. You need to have a seat on the table," says Dr Satya Gupta, President, VLSI Society of India.

Building the ecosystem: There's more than what meets the eve

"I think that if there is any nation that can emerge as a superpower in chip design, along with China, India has to be there," notes Dr Naveed Sherwani, CEO, Rapid-Silicon. The road to the zenith, however, is a long and risky one, something Indian businesses are yet to be ready for. In our bid to achieve self-reliance, big corporations are



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necessary to give our mission the momentum, but the ultimate goal must be reached by homegrown chip companies.

It is a widely known fact that making chips is a precarious business, which needs steep investment. It becomes even more tedious when a new startup wants to enter the space thanks to the high cost of entry. "The first challenge these startups face is the cost of EDA tools which are prohibitively expensive. Almost 25% of the total cost of the entire design chain is attributed to the cost of the EDA tools," says Vivek Khaneja, Executive Director, C-DAC.

And although this is being addressed to some extent by open source tools and open domain companies, which help provide easier access to EDA tools and IPs, the truth is that their standard offering is beyond the reach of a small startup.

Availability of IPs is another major obstacle that prevents chip startups from moving forward. When you are trying to solve a problem, you will create a solution that has some sort of programmability and peripheral IPs. Getting hold of those IPs, which are silicon proven today, is not easy and, most importantly, it is not cheap.

Open source IPs have made life easier but only on the surface. At the crux of it, the quality of such IPs may be subpar and integrating those IPs in the design is a matter of shooting in the dark with the hope of striking Eureka.

Additionally, building a prototype for a chip startup is extremely difficult due to their limited funding and the fact that, typically, foundries refrain from giving them much leverage when it comes to prototyping.

Mike Wishart, CEO, e-Fabless, points out, "You look at what happened in software, by removing all the upfront cost, they took out the 'no' out of innovation. One of the

biggest issues you get in silicon is that there are so many things you have to spend money on."

Funding for chip startups

"Based on my experience of raising money for my own company, a big part of getting companies funded and up and running is, in fact, persistence," explains Wishart. Hardware startups are notorious for being incapable of catching the eye of investors due to the high investments that they require along with the even higher risks and marginally lower profits associated with them.



When you come back to the life cycle of funding a company, it is a 'crawl-walk-run' cycle for every company. The crawl part is when you have to get the proof of concept, and then come the walk and run part when you produce it and then scale it up. "The funding difference between software and semiconductor is not that different in the walk and run part but it's the crawl, the getting started part, which is the killer," says Wishart.

The existing semiconductor ecosystem is built and consolidated around creating and making standard product chips for mass markets, where the basic model is to pour tremendous amounts of money into capital equipment and R&D, create a product and sell a lot of them to enable PCs and phones. Industry leaders emerged in areas like EDA, IP, and foundry and they quite understandably protected their investment licenses that must be signed and often paid for upfront. As a result, not only did it get more expensive to

start a company, but it also became incredibly time consuming.

The emergence of open source led to a great amount of costreduction along with simplification, sharing and faster time to design. Yet, their delivery for chips that are optimised for quality, power, performance, and cost is often not at the level required for startups to be able to scale their product. It gives the required push, but it may not be sufficient to keep the momentum.

Building a company from the ground up is a tedious task. As such, building a chip startup is difficult on its own. Moreover, the chip ecosystem is an extremely rigid one that leaves very little space for newcomers. How do we turn the tide?

"I think the tide will only be turned if there is a demand for locally produced chips," says Sherwani. While the government's efforts to support domestic startups to encourage them to build chips is commendable, there is a need for these chips to come to the forefront, which can only happen when enough demand is created.

Sherwani suggests having provisions that may help incentivise the use of locally-made chips.

"I'm not saying putting a barrier for big players, but indigenous players need to be given more benefits. As part of this overwhelming plan, we must create incentives for startups," Sherwani adds.

Lack of VC infrastructure

The lack of a proper venture capitalist (VC) or venture capital infrastructure is another obstacle in allowing proper funding to flow in the right direction when it comes to hardware startups. A lot of VCs that exist in the country do so only on the surface level. At the core, these are just US based VCs who operate in India, among other places. The need, in this case, is much more precise—India needs Indian VCs to understand the local demand.

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-Authors: Asher Vinod along with inputs from Prashant Kalalabandi, El Camino Technologies Pvt. Ltd.



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The age of Al and future of EDA

While all this chatter is taking place, software companies have also started realising the importance of chips. Marking their own presence, companies like Google have begun making their own chips and even released an Al based floor planning tool.

Al has taken the world by storm, having been incorporated into every little aspect of our life. It is only natural that the chip industry would get the taste sooner or later. Will this change the EDA space? Will Al take over the chip making process, eliminating the need for human intervention forever?

While tools like these are a welcome innovation in the industry, even by staunch hardware guys, the fact remains that removing human intervention completely is yet a far cry. "They will be helpful for simpler chips. For more complex chips, human ingenuity is still needed. A master floor planner is still needed but the iteration we go through can be helpful for more productivity," says Sherwani.

But one of the biggest aspects that the entry of AI would change is the access to EDA tools, especially for companies that have limited resources to access world-class tools. "I think this is one thing that EDA companies have resisted to this day. If the tools move to cloud, then hooking it up with the additional tools on the cloud will really help everyone all over the world. But we need to have a wider ecosystem and AI will provide that, I think it could be a good step," adds Sherwani.

and fuel their capital to meet those needs.

With the semiconductor policy in place, there is hope for such an infrastructure to be built. But that needs a lot of push to come from the industry to show how marketable their product is, whether in local or global terms, in order to attract the right set of eyes towards them and let the pendulum of reverse brain drain going.

A common problem that many tech startups face is the lack of business acumen. No doubt intensive technical knowledge is critical, but the lack of decision making collapses a business even before it begins. And the problem is a deep-rooted one.

While a lot of design moved to India, design decisions never moved to India. Big MNCs may have 10,000 people working for them in India but no decision makers are here. The people who are buying chips are mainly in the US and, as such, one key thing that never happened is that chip buyers and people who specify what chips need to be designed and how they need to be designed were ever here in India.

This lack of critical decision making has led to the workforce in India to be unaware of the specific market demands. And when this workforce starts out on its own, it is only natural that their product does not fit market standards. This becomes another hurdle in getting the right funding where the VC's expectations and the product just do not match.

Leveraging the academia

A large part of Silicon Valley's success is attributed to the contributions made to innovation and collaboration of the academia and the industry.

Subhasish Mitra, Professor at Stanford, says, "There are three key aspects in which US universities have made a big mark. They are not all Silicon Valley aspects, which is why I believe we can emulate the same in India because I think the expertise that is needed in each of these areas, India has that."

The first aspect, Mitra reiterates, is the ideas that come out of academia. The way this happens is that they pick a problem that, if solved, would have a big impact.

Second, there needs to be a mindset shift to not just focus on creating specific chips but also create the fundamental technologies to figure out how to create the next technologies that will enable the

next generation of chips. Here, too, the formula is the same—you pick a problem that is really important and try to find an end-to-end solution.

Third, it is necessary to give due focus to the electronic design automation (EDA) aspect. The innovation that happened in that aspect was, again, they followed the same formula of problem solving.

Mitra asks, "Why can't India do all of this? Because if you look at the expertise in India, Indians have creativity that is directly connected to chip design and at the same time Indians are very good at math. So, there is no reason why India should not shine there."

But while ideas from academia are long-range and industry changing, they also require heavy investments and have long gestation periods to make them see the light of the day.

"In the US, we have been able to fund these ideas and it may take a company 9-10 years before that product hits the market, but it changes the world. We don't have that kind of patience, which is why I think India needs something different," says Sherwani.

This, suggests Sherwani, can be solved by focusing on building low-cost, high-efficiency chips, for which the country already has existing capabilities and infrastructure.

The problem, however, lies in striking the right balance. While focusing on such chips would help light that fire and kick that ball, there is a danger of that ball being stuck in a local minima. As such, the focus would only be limited to solving a local problem and may never reach global scale.

This is where it becomes imperative to strike the right balance between encouraging and fueling academic research, while also focusing on keeping India on the right track to achieve reliance and sustainability in chip design.



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Dr Satva Gupta, President, VLSI Society of India



Subhasish Mitra, Professor at Stanford, the USA



Vivek Khaneja, Executive Director, C-DAC

India first, world second

It is no surprise that the golden period for Indian electronics and semiconductor industry has started to take shape. Sherwani notes, "What we now need is a group of people, Indians who want to make India great. India has a very large number of people with extensive experience in various aspects of chip making. How many nations have that?"

Fostering this ripe talent and providing them avenues to come and build the semiconductor industry is essentially among the best ways to develop the same. Especially since most of these talents have worked closely with companies based in Silicon Valley, they have the knowledge and expertise to emulate the same in India. The product ecosystem in India is already well-developed, what it needs is a little fire to make it shine bright enough for the world to see.

The chip industry is a cutthroat one, and India being in its nascent stage to develop its own niche, needs to be wary of diving headlong into the competition. While the ambition is understandable and lauded, common sense must still prevail if it wishes to make a mark. For that, it needs to turn its head and start looking inward rather than outward. "I think we must start with making simple chips, chips which are relevant to India," says Sherwani.

Microcontrollers, IoT chips, and LED PCBAs are some aspects that India can aim for, simply because their demand in local terms is extremely high, making it easily scalable while also giving the still-growing market a big indigenous push. Gupta suggests taking a thread out of the big-little approach that many MNCs have followed. This would allow Indian companies the option to choose indigenously produced chips to add to their domestically produced products, which would further increase their value addition.

"For instance, India today makes 700 (types of) LED bulbs domestically for domestic consumption. A small tensor chip goes into every small volt LED. It's a simple chip but the volume is humongous. And

since the product is also made by Indian companies, they can decide what to use. This could be the little part of the big-little. The big part would automatically proceed once we conquer our local ground," says Gupta.

Additionally, owing to the high investment that chip making requires, not every idea gets elevated. This also means that a lot of great ideas with little marketability, at least initially, have had to go down the drain only because someone decided it would not be worth spending on.

"I think we need to become more open and allow for more people to contribute their ideas in order to see what kind of stars can emerge. It is important to remove the ability of third parties to say 'no' to a project as much as we can," Wishart adds.

Grabbing the seat

The government's strong foot forward with regards to the semiconductor space was a much-needed move and, although late, it is expected to herald the golden era of the electronics industry of the country. What needs to be done is to adopt a problem-centric approach to think of an end-to-end solution, which can be scaled up from a local problem into a global problem. This is where the academia needs to play its part.

"The real question is how we can get real good ideas. The moment we are able to bring compelling ideas, people will be forced to fund them," reiterates Mitra.

While sovereignty is the ultimate goal, India needs to focus on getting a share of the pie, no matter how small it is. In the end, India needs to do enough to sit on the table with other countries, because if we control even 3% of the market, we can sit there. If we are zero, we will not even be invited. EFY

> The author, Siddha Dhar, is a technology enthusiast at EFY



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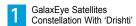
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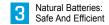
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Zypp's Unique Solution



GalaxEye SATELLITES CONSTELLATION WITH 'DRISHTI'

GalaxEye, an IIT Madras incubated space tech startup, aims to send a constellation of satellites equipped with their proprietary sensor, called Drishti, into space



GalaxEye team with some of their mockups

The founders of GalaxEye have developed and launched the Drishti sensor, a patent-pending technology that is also their USP. "The Drishti sensor enables multisensor fusion," says Suyash Singh, Co-Founder and CEO, GalaxEye. In simple words, Drishti sensors are basically a hybrid version of multiple sensors-hence, multi-sensor fusion. There are multiple sensors today that are used on an individual basis, like thermal sensors, radiometers, or radars on a satellite. But

GalaxEye's Drishti sensors are able to give comprehensive data that anyone would want from a satellite since they are a combination of many sensors.

The Drishti sensors themselves are not commercially available. They will be deployed on GalaxEye's satellites only. What the company will be making commercially available is the dataset obtained from the sensors. GalaxEye believes that this dataset will be able to serve any sector that requires satellite

imagery—insurance, agriculture, utilities, defense, etc.

GalaxEye's mission is to launch a constellation of satellites working in tandem to provide imagery. "We will have a group of such satellites monitoring the Earth on daily basis," explains Singh. This constellation will provide insights both during day/night time and irrespective of weather conditions. However, their short-term goal is to launch their first satellite in some time, probably by the end of 2023.

It is well said that big success does not come without challenges at times, the volume of the data that needs to be transferred from one point to another is challenging. GalaxEye is solving that challenge by building a dashboard with a map for users to draw a cross-sectional area on. The company will then deliver the data for that particular area.

Dealing with market competition is critical for all companies. Even in the space technology business, there are some global players who are doing satellite imagery using single sensor based imaging. What differentiates GalaxEye from them is the multi-sensor fusion mentioned earlier. It is this differentiation that they bring to the table that's very important for the customers.



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GalaxEye, like most startups these days, has a very unique story of how it was launched. In 2020, the company was founded by a team of five students/alumni of IIT Madras, who initially were a part of Team Avishkar Hyperloop—a student competition team. In 2019, the team participated in the SpaceX Hyperloop Competition and that's

how GalaxEye came into being. "A few of us from the Hyperloop team were really excited about space technologies in general," recalls Singh.

Singh points out that the Indian government, ISRO, and some universities have been taking steps towards getting students interested in space technology—like launching student space programmes and interdisciplinary courses. He throws light on how students can get started in a niche field like space technology, "Students can start with drones and communication, and then, if interested, they can move forward to taking specialised subjects during the PG."

—Aaryaa Padhyegurjar

ZYPP'S UNIQUE SOLUTION FOR EV FLEETS

Many Indian startups have begun dabbling in the electric vehicle (EV) space. While most of them follow the same route with their business, Akash Gupta-led Zypp Electric has gone out of the box and risked a fusion that very few have executed successfully

hether due to government's mandate, or the intention to reduce their carbon footprint, businesses all over are actively looking to make the shift to EVs. Logistics sector, which is notorious for carbon emissions, urgently needs to make this shift and Zypp Electric has taken note. It leases electric bikes complete with



(L to R) Tushar Mehta (Co-founder & COO), Rashi Agarwal (Co-founder & CBO), and Akash Gupta (Co-founder & CEO)

Akash Gupta, Co-founder & CEO riding Zypp Electric

battery infrastructure and maintenance support to such companies so that they need not worry about investing large sums on buying EVs. Their platform helps to keep a check on these e-bikes and also operate them.

Zypp also smartly prevents theft of the

e-bikes. Apart from the usual KYC of the driving partners, these e-bikes have been fully tech-enabled. There is no key and the bikes are geofenced to always track them. Their custom battery management system ensures that the batteries are utilised properly. "We have proper triggers baked onto the app, which tells the driver the right time to charge the bike," explains Gupta, CEO and Co-founder, Zypp Electric.

But that's not all. Zypp has also



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figured out how to fund the acquisition of these bikes for themselves. "All of these bikes are invested by HNIs who like to invest in these assets. It is like an alternate asset/ investment earning plan similar to how you invest in an FD or a mutual fund or real estate." Zypp offers 20% pre-tax return to these investors for a 3-year plan, after which it offers to buy them back, essentially turning itself into an EV fintech firm!

In terms of scaling up, Zypp's

ambitions are simple. Expansion. Be it expanding the number of bikes they currently deploy (which is pegged to reach 15,000 within 12-18 months), or increasing the number of clean deliveries happening in the country, or expanding their operations from Delhi-NCR and Hyderabad to Bengaluru and Mumbai, Gupta aims to do it all.

This is not easy though; the biggest hurdle in Zypp's plans is the low number of bikes being supplied at present. "The EV ecosystem and

OEMs are still in the nascent stage, so none of the OEMs are able to deliver 1000-2000 bikes a month today, even though we are ready to buy those many bikes every month."

Additionally, Gupta wants more robust bikes to be made that are suitable for the Indian terrain. Zypp's outlook is positive, and it believes that with the right nudge, it can propel the country into meeting the clean energy goals it has set.

-Siddha Dhar

NATURAL BATTERIES: SAFE AND EFFICIENT

While recent incidents of electric vehicle fires have alerted us on battery safety needs, a Jaipur based startup, Natural Battery Tech, is building batteries that it claims can sustain harsh temperatures up to 200°C and still provide optimal efficiency

ith the rise in demand for larger battery packs with more safety features, Puneet Jain, CEO, Natural Battery Tech, says they are now designing lithium-ion batteries that can work beyond their rated capacity and also sustain harsh temperatures.

"The lithium cells (NMCs) may get heated and touch up to 55-60 degrees Celsius, leading to thermal runaway. The usual batteries available in the market may use a thermal foam or epoxy sheet that can protect up to 100 or 125 degrees Celsius. By using specialised thermal pads and heat protection devices, we are able to secure the complete pack up to 200 degree Celsius. These materials can also absorb vehicle shocks to safeguard battery internal components," he explains.

For ensuring safety from faulty cells, the firm has introduced fused connection technology, which isolates the faulty cell in the pack from the rest. "A battery pack is a combination of cells. If you are using a



simple connection type and if any cell goes bad, it cannot isolate itself from the regular pack. While bigger

firms like Tesla go for expensive technologies like ultrasonic bonding to address such issues, we have

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66



-Jeff Bezos



come up with a cheaper solution of using fused connections for our battery packs," says Jain.

Jain calls the optimisation feature as their unique selling point. "If a client orders a 50Ah (amperehour) battery, we give them a 51.5Ah battery. This is a subtle difference, but it turns out to be a great trust-building factor," he says.

To optimise space and fit more battery capacity in limited vehicle space, the firm uses customised ABS structures to keep the lithium cells in place. The ABS structure has involved careful selection of materials that can enable the battery to be compressed further and allow packing more capacity. A typical battery of 50Ah capacity is housed in a box of 400mm thickness and with the compression structure, it is reduced to $365 \sim 370$ mm. The space saving (approx. 30mm all-round) can be used for increasing the capacity of the battery further.

While the team had earlier faced difficulties in selecting materials that could provide both thermal and shock protection, and also in designing ABS structures, finding a manufacturer in India was a bigger challenge. "We couldn't find a manufacturer in India and hence we moved to importing from countries like China and Germany. This is also a reason that puts our cost a little higher compared to our competitors," Jain says.

The business was started in 2019 with the leasing of electric rickshaw batteries. It was later shifted to a couple of other financial models, realising the initial heavy investments. "I partnered with electric vehicle manufacturers and sometimes we even did the delivery service by hiring drivers. All those options were not very scalable with the limited funds. Now we currently focus only on building batteries," he adds.

He strongly believes that allow-



Puneet Jain, CEO, Natural Battery Tech

ing a free hand to experiment is a great way to innovate. "I have quite some examples in our factory where we had trained our manpower from little technical expertise to adapt successfully in innovating at our factory floors. Those with not so higher education backgrounds have experimented a lot with testing, repairing, tinkering, and are now working out ways of producing efficient results," Jain says.

Jain expects to soon introduce an effective liquid cooling mechanism. His team is designing the pump and pipe systems for liquid cooling to effectively keep the larger packs cool. Jain aims to develop a great battery ecosystem deploying battery swapping and charging infrastructure in India in the long run. He says, "We are already in talks with some investors. We want to create an infrastructure where the batteries can be easily serviced, recharged and all managed through mobile apps."

The firm is currently planning expansion and looking for sophisticated OEMs to co-design battery packs customised for their vehicles. It is also in process of hiring battery researchers, mechanical engineers, supply chain managers, product development experts, and electrical engineers.

—Abbinaya Kuzhanthaivel



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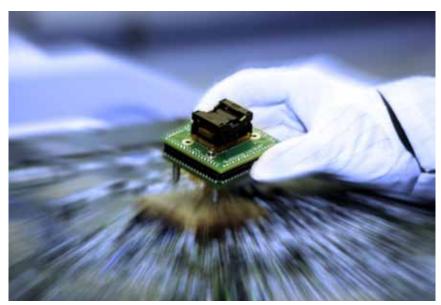




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MeitY forms 17-member advisory committee to steer Semiconductor Mission

The Ministry of Electronics and Information Technology (MeitY) has announced the formation of a 17-member advisory committee to provide a more solid path to the government's ₹760 billion India Semiconductor Mission. The Minister of Electronics and Information Technology Ashwini Vaishnaw will be the Chairperson of the committee, while Minister of State for Electronics and Information Technology Rajeev Chandrasekhar will be the Vice-Chairperson. The newly minted committee shall provide inputs to the government to enable the building of a resilient supply chain, promote investments and ways of financing the semiconductor sector, research and innovation as well as intellectual property generation.



Representational image

PEOPLE (on the move)



Sumant Sinha takes over as ASSOCHAM President



Sumant Sinha (left), Founder, Chairman & Managing Director of ReNew Power, has taken over as President of the Associated Chambers

of Commerce and Industry of India (ASSOCHAM). Ajay Singh (right), Chairman & Managing Director of SpiceJet, India's second-

largest airline by fleet size, is the new Senior Vice-President of ASSOCHAM.



Rajan Kapoor appointed NexGen Power Systems' MD for India R&D

NexGen Power Systems has appointed Rajan Kapoor as Managing Director for its India operations. As Managing Director, India, Rajan will be responsible for driving NexGen's India Centre of Excellence to revolutionise the design

and development of power conversion systems.

SECI appoints Sanjay Sharma as Director-Solar Division

Sanjay Sharma has been appointed Director of the solar division of Solar Energy Corporation of India. Prior to this appointment, Sharma held the position of Executive Director of the state-run company.



Tushar Mehta appointed Zypp Electric's COO



Electric mobility start-up Zypp Electric has elevated its Business Head, Tushar Mehta, as the company's Co-founder and Chief Operating Officer. Tushar joined Zypp last year as Business Head and has been closely working with the founding team to improve operations

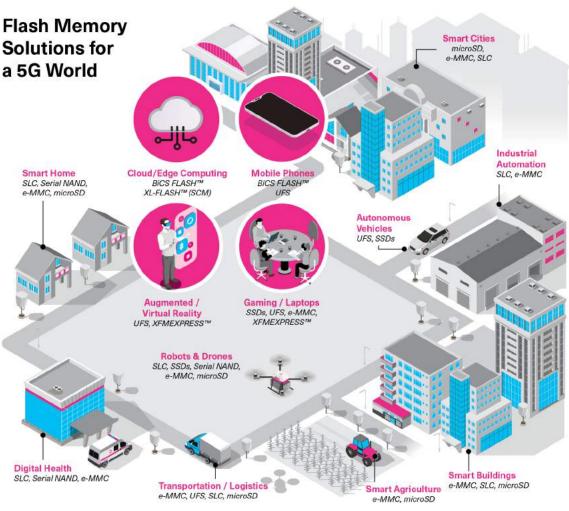
and business strategies. In his new role, Tushar will be leading the business and growth at Zypp.

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CORPORATE News



Motherson Sumi Group eyeing to foray into semiconductors

Following the likes of Vedanta and Tata, Samvardhana Motherson Sumi Group is also planning to enter the semiconductor manufacturing space. It is looking for a global partner to enter the same. Motherson's Vice-Chairman Laksh Vaaman Sehgal said that the investment to get into semiconductor manufacturing being large, the company will not get into the space on its own and is eyeing a

Tejas Networks to acquire majority stake in chip firm Saankhva Labs

Telecom gear maker Tejas Networks is acquiring 64.4% stake in Bengaluru based semiconductor firm Saankhya Labs Private Ltd in an all-cash deal of ₹2.84 billion. Tejas Networks, upon procuring all necessary consents and approvals, also intends to proceed with acquiring the balance 35.6% shares through a merger process or a secondary acquisition. Saankhya develops system and semiconductor products for cellular wireless, broadcast radios, and satellite communication ground terminals, which are deployed by customers in India and in international markets. It has 73 international patents.

Samsung now eyeing PLI incentives through contracts

Samsung Electronics, which had previously refused to participate in the telecom production-linked incentive (PLI) scheme, is in talks with Indian telecom majors Reliance Jio and Bharti Airtel for 4G expansion and 5G supply contracts. Samsung had in mid-2021 decided not to take part in the initial PLI scheme, reasoning that setting up a plant for network equipment was not viable at that time in India with just one client, Reliance Jio Infocomm.

Made in India iPhones: Pegatron and Foxconn are on the roll

Apple's second largest contract manufacturer Pegatron has local manufacturing of the tech giant's products in India, with a target to produce goods worth more than ₹80 billion in the next fiscal year. It will initially make iPhone 12 devices, followed by the new iPhone 13 series from a new facility in Tamil Nadu. Meanwhile, Foxconn has already started making iPhone 13 in India.

Vikram Solar set to file papers for an IPO

Vikram Solar is looking to go public as it files draft papers for a reported ₹20 billion initial public offering (IPO). It will offer a mix of fresh share sales and stake sales by promoters for the issue, which is likely to be worth ₹1.8 billion. It has roped in JM Financial and Kotak Mahindra Capital as the lead managers for the public issue.

Tata Motors to make massive investment in EV segment

Tata Motors is planning to invest ₹150 billion in the EV segment in the next five years. President for passenger vehicles business of



SNIPPETS

- · One Moto is looking to set up an EV manufacturing facility at Zaheerabad in Telangana with a manufacturing capacity of 25,000 units per month, which will be ramped up to 100,000 units in the next three years.
- Pinnacle Industries has introduced a range of precision EV components for two- and three-wheelers as it looks to become an end-to-end EV components and solutions
- Spacetech start-up Pixxel has raised \$25 million in a Series A funding round led by Toronto based Radical Ventures, making it India's largest fundraise by a space technology company to date.
- EV mobility firm Bounce is looking to expand its presence in the EV industry of the country, for which it is eveing a fundraiser of \$100 million.
- Domestic electronics maker Blue Star E&E has opened a new medical diagnostic equipment refurbishment facility at Bhiwandi, Maharashtra.

Tata Motors Shailesh Chandra said that the company is also planning to develop around ten more new offerings in the seg-

Suzuki to invest \$1.4 billion in Gujarat to build EVs

Gujarat is set to become a hotspot for electric vehicle (EV) manufacturing as Japanese auto major Suzuki Motor Corporation is looking to invest approximately 150 billion yen (₹104 billion) for local manufacturing of EVs and their batteries. The company signed an MOU with the state of Gujarat at India-Japan Economic Forum held in New Delhi, in the presence of Japanese Prime Minister Fumio Kishida and Indian Prime Minister Narendra Modi.

Omega Seiki Mobility to enter African EV market through JV

Looking to expand its reach to the African market, Omega Seiki Mobility (OSM) has joined hands with Egyptian e-commerce lastmile delivery firm Rabbit Express to form a joint venture for the same. OSM will initially export its two- and three-wheelers to the African market and later the two partners will set up a manufacturing facility in capital city Cairo. OSM will hold up to 26% stake in the JV while Rabbit Express will hold the remaining stake.

US based Triton Electric to manufacture EVs in Gujarat

With a total investment of ₹108 billion, US based Triton Electric Vehicle has signed an agreement with the Gujarat government to set up a commercial electric vehicle (EV) manufacturing plant at Bhuj in Kutch district. Triton Electric Vehicle will set up the plant over 645-acre land with an annual production capacity of 50,000 trucks. The state government will assist in providing necessary approvals and registration for the establishment of this plant in accordance with the existing policy.

ITP starts lithium battery manufacture in Gurugram

ITP Energy Systems has set up a 100MWh lithium battery manufacturing plant in Gurugram. In phase 1, the facility will cater to solar segment at first and then focus on battery packs for e-mobility, which are due for launch in Q3 FY22. With multiple EVs catching fire in the past month, the plant at Gurugram has been set up, keeping this in mind, to make batteries that are thoroughly tested and have proper thermal management.



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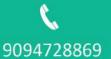
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Ola Electric invests in Israeli battery tech firm

Electric vehicle giant Ola Electric has invested in the Israeli battery technology company StoreDot. StoreDot makes batteries with extreme fast charging (XFC) technology. As part of the investment in StoreDot, Ola Electric will have access to the company's XFC battery technology, along with exclusive rights to manufacture batteries integrating StoreDot's fast charge technology in India.

Cygni Energy looking to set up lithium-ion battery plant

Hyderabad based lithium-ion battery pack manufacturing firm Cygni Energy Private Limited is looking to invest about \$40 million (over ₹3 billion) to set up a greenfield manufacturing plant near Hyderabad. Their batteries are used for electric vehicles and stationary applications like telecom towers.

Exide building Li-ion cell manufacturing unit in Karnataka

Battery maker Exide Industries has announced it is planning an investment of about ₹60 billion in building a lithium-ion cell manufacturing unit in Karnataka. The company has sought 80 acres of land at Haraluru industrial area near the Bengaluru international airport. This proposal is for one of India's largest giga factories for advanced cell chemistry technology.

Epsilon Carbon signs JV for battery materials plant in India

World's first commercial polymetallic nodule processing plant is coming up as Epsilon Carbon as signed a joint venture agreement with US' Nasdaq-listed The Metals Company Inc to invest ₹12 billion for it in India. The focus of this collaboration will be to produce cathode precursors with the lowest carbon footprint. These cathode precursors will be converted to cathode active materials at the proposed facility.

Simple Energy signs MoU for Li-ion battery cells ecosystem in India

Simple Energy has signed a Memorandum of Understanding (MoU) with US based battery technology firm C4V for setting up

lithium-ion cell manufacturing ecosystem in India. In addition to the make in India initiative, this strategic partnership covers cells with industry-leading safety, higher energy density than lithium ferro phosphate batteries, faster charging, and longer life cycle based on C4V's patented technology.

BEL and AAI join hands to develop air-traffic management systems

Bharat Electronics Limited (BEL) and Airports Authority of India (AAI) have entered into an agreement for the joint, indigenous development of systems for air-traffic management and surface movement of aircraft at airports in the country, which were hitherto being imported. BEL and AAI will jointly develop civil airtraffic management system with advanced surface movement guidance and control, a complex ground surveillance system that manages air-traffic at airports and in Indian civil airspace for safe operation of flights from take-off to landing.

Samsung to re-enter India's laptop market after exit in 2014

Sensing a rise in demand in the market, Samsung is all set to re-enter the Indian laptop segment this month as it looks to capture double-digit market share by the year-end. Samsung India General Manager and head of new computing business Sandeep Poswal said that by the end of 2022, Samsung plans to have double-digit market share in the addressable area where it operates.

Nothing India to begin making smartphones

Carl Pei-led Nothing is now looking to make smartphones. It is planning to foray into the business in the third quarter of this year, Nothing India Vice-President and General Manager Manu Sharma revealed. Pei, who is the Co-founder of OnePlus, said that Nothing is developing its own operating system for the phones, which will be built using Qualcomm's Snapdragon mobile platform.

Centre plans to modernise SCL plant for chip manufacture

As part of its effort to begin manufacturing semiconductors, the

Semi-Conductor Laboratory (SCL) in Mohali is being modernised by the Central government. The Parliamentary Standing Committee on Communications and Information Technology has submitted a report, which

gives a detailed plan for modernising the SCL unit in Mohali. The government had asked the committee for a plan to upgrade the SCL factory. The India Semiconductor Mission (ISM) will be responsible for handling the upgrade.

Smartphone exports increase 75% in FY21: ICEA

The India Cellular & Electronics Association (ICEA) has released data which suggests that smartphone exports increased by 75% in financial year 2021, reaching over \$5.5 billion from \$3.16 billion. ICEA attributed the IT hardware productiion-linked incentive (PLI) scheme as the primary driver of this growth.

Chennai now the best for low-cost electronics R&D

Chennai was ranked as the world's cheapest foreign direct invest-

	BATTERY STORAGE PLI APPROVAL FOR INCENTIVES
--	---

Name of applicant	Capacity quoted (GWh)	Status	Capacity awarded (GWh)		
Rajesh Exports Limited	5	Awarded	5		
Hyundai Global Motors Company Limited	20	Awarded	20		
Ola Electric Mobility Private Limited	20	Awarded	20		
Reliance New Energy Solar Limited	20	Awarded	5		
		Waitlisted	15		
Mahindra & Mahindra Limited	15	Waitlisted	_		
Exide Industries Limited	6	Waitlisted	_		
Larsen & Toubro Limited	5	Waitlisted	_		
Amara Raja Batteries Limited	12	Waitlisted	_		
India Power Corporation Limited	5	Waitlisted	_		



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INTERNATIONAL NEWS

Intel to beef up its investment in Europe for making chips

US chipmaker Intel has revealed that it will invest up to 80 billion euros (\$88 billion) across Europe in the next decade, as part of which it plans to build a semiconductor plant in Germany. The chipmaker will allot 17 billion euros to beef up its European production capacity with a semiconductor fab mega-site in Magdeburg, Germany. The site will include two semiconductor factories, or fabs, that will make chips with Intel's advanced technology.

Nvidia interested in using Intel for manufacturing its chips

Following the fallout of its deal to acquire British chip firm ARM Ltd, Nvidia has shown interest in partnering with US chipmaker Intel to make chips. Early last year, Intel decided to expand its business into making chips that others design as well, called the foundry business, and has announced several multibillion-dollar projects for new manufacturing centres in the United States and Europe.

SK Hynix looking to acquire Arm through a consortium

Months after the fallout of the highly-anticipated Nvidia-Arm deal, South Korean chipmaker SK Hynix is looking to form a consortium to acquire British semiconductor company Arm. "We are reviewing possibly forming a consortium, together with strategic partners, to jointly acquire it," Park Jung-ho, Vice Chairman and CEO of SK Hynix, told reporters after the company's annual shareholders meeting. The idea of possible acquisition is, however, at an early stage.

Kioxia and Western Digital to build new flash memory fab

Kioxia Holdings Corp is looking to collaborate with Western Digital Corp to build a new flash memory fab together. The facility is set to be bult at the latter's plant in northern Japan, which may reportedly entail an investment of around \$8.3 billion.

Samsung and Western Digital to develop next-gen storage

Samsung Electronics has signed an MoU with Western Digital to co-develop next-generation data placement, processing, and fabrics (D2PF) storage technologies. The companies will initially focus on aligning their efforts and creating a vigorous ecosystem for zoned storage solutions, including zoned name spaces SSDs and shingled magnetic recording HDDs.

SEMIKRON and Danfoss Silicon Power merge to specialise

SEMIKRON and Danfoss Silicon Power announced a merger to create a joint business specialised in power electronics focusing on power semiconductor modules. The newly formed SEMIKRON-Danfoss joint business will be owned by the current owner-families of SEMIKRON and the Danfoss Group, with Danfoss being the majority owner. The new joint business will be managed by one common leadership team, retaining existing production facilities, customer and supplier relationships, and distribution channels.

US chip firm Ampere Computing files for IPO

American server processor startup Ampere Computing has filed for an IPO in the United States. A public listing would give Ampere an infusion of cash and potential access to more investment further down the line via public markets. Oracle has quietly invested \$426 million in Ampere, which is run by former Intel president Renée James.

Wacom, STMicroelectronics and CEVA collaborate for digital pens

CEVA, together with STMicroelectronics and Wacom, have announced their collaboration to develop an enhanced digital pen experience with a new, wireless sensor module that extends the digital pen's functionality through advanced gesture, cursor, and motion control. The combined effort leverages the specialised capabilities of the three companies to create an advanced, sensorenabled digital pen that can be adopted by OEMs to add value to their smartphone, tablet, notebook, PC, interactive whiteboard, or other smart-display products.

Analog Devices and Gridspertise join forces for smart grids

Analog Devices and Gridspertise are collaborating to advance the resiliency and quality of smart grids around the world. The collaboration enables the development of new hardware and software that support distribution grid self-healing and adaptation in response to the significant changes in energy supply and demand as renewable energy sources are brought online.

ment (FDI) location for electronics research and development, as per an assessment conducted using investment location comparison tool called fDi Benchmark. Chennai emerged as the cheapest location in the top hundred electronics FDI destinations, with estimated annual operating costs of \$1.24 million for a fifty-person R&D centre. Among the world's competitive locations, Chennai was ranked second, only after Seol, which is home for large campuses of players like Samsung and LG Electronics.

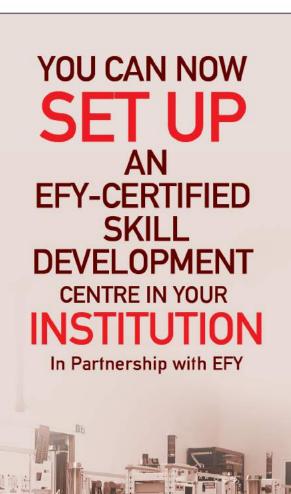
Niti Aayog to roll out battery swapping policy

Keeping in line with the Finance Minister's promise in the Union Budget speech, NITI Aayog is likely to roll out a battery swapping policy by December this year. "The Battery Swapping Policy is likely to be rolled out by December," Sudhendu Jyoti Sinha, adviser (Infrastructure Connectivity-Transport and Electric Mobility), NITI Aayog, informed. Sinha also said that the Aayog is working on a proposal to prod railways to set up electric vehicle

charging stations at railway stations in cities whose population is more than one million.

Government begins probe as more e-bikes catch fire

Following recurring incidents of electric bikes catching fire, Road Transport and Highways Secretary Giridhar Aramane told media that independent experts in science and technology space will probe the incidents and submit a report. The government has ordered a probe into the matter.





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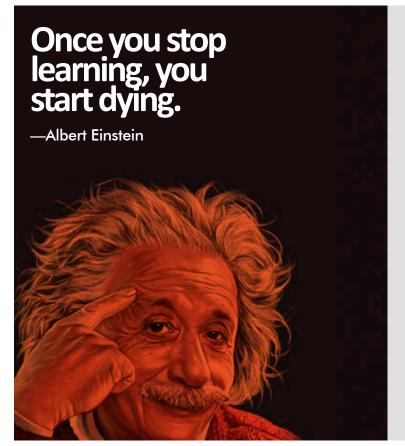
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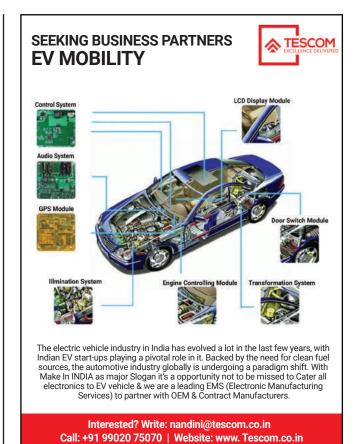
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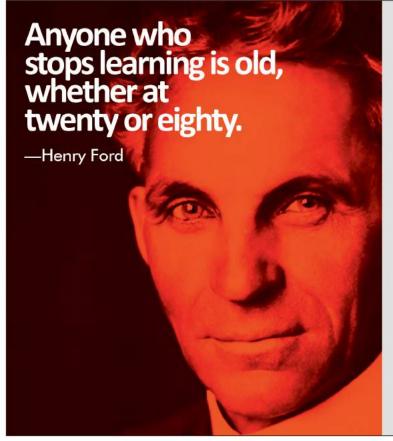
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AI HARDWARE That's Tailored For COMPUTER VISION

Neural networks, Al, ML, and neuromorphic computing are technologies that are actually ways to make machines behave more like humans! Similarly, computer vision focuses on imitating the complex human vision system and leveraging it in applications

AARYAA PADHYEGURJAR

omputer vision is a type of artificial intelligence (AI). The aim is to make computers visualise and understand images or videos as humans do. The field has grown a lot over the last decade, especially due to the new hardware and algorithms that came into the picture. Not just that, this type of computation has become faster and more accessible since the amount of data that is generated has also increased. Thanks to improved training models, deep learning, and better hardware, we are able to identify objects more accurately today.

Computer vision works by recognising patterns in images and videos. You feed the

computer with an image of a particular object and then make the computer analyse it using algorithms—the borders, colours, shades, and shapes. We are essentially training a computer to understand and interpret the world.

Computer Vision at Work!

For the 2018 Masters Golf event, IBM employed computer vision to produce My Moments. IBM Watson was given hundreds of hours of film, after which it was able to recognise the sights (and sounds) of key scenes. These pivotal moments were chosen and distributed to viewers as individual highlight clips!



Drone Skygrid, an airspace management system built on AI and blockchain, uses computer vision for near realtime object detection through a drone's live video stream (Credit: Skygrid)



Computer vision: Then and now

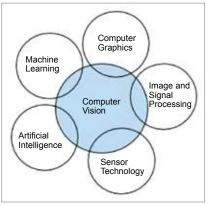
In his whitepaper titled 'Everything You Ever Wanted To Know About Computer Vision,' Ilija Mihajlovic, an iOS developer who is passionate about machine learning (ML), explains how ML and deep learning have simplified computer vision. Before deep learning came into the picture, computer vision did not have as much scope as it does today, mainly because a lot of manual coding was required, such as creating a database, annotating images, capturing new images, etc.

"With machine learning, developers no longer needed to manually code every single rule into their vision applications. Instead, they program features or smaller applications that could detect specific patterns in images," writes Mihajlovic. Today, we use deep learning for computer vision, and all of the work is done by neural networks. When we provide neural networks with lots and lots of labeled images of a particular object, they are able to identify patterns in them.

Hardware for computer vision

Capturing data is an important aspect of computer vision applications, and hardware platforms play a major role here. The main components required for computer vision applications are cameras and image sensors for capturing images, I/Os, a communication interface, and a processing unit. Imaging sensors present in cameras are characterised by their resolution (number of pixels), speed (frames per second), and number of colours.

Depending on the type of



Fields that drive computer vision (Credit: IFA)

computer vision application, the processing unit might vary from CPUs to embedded boards. Here are a few examples:

- CPUs
- GPUs
- Heterogenous systems (CPUs
- + GPUs + others)
 - FPGAs
 - ASICs
 - Microcontroller boards
- Embedded systems (smart cameras)

So where do development boards stand?

Traditional computer vision systems used a camera connected to a general-purpose computer, which used to run the application. In a smart camera though, the whole system can be integrated, right from capturing the image to processing to the final application. Today, the distinction between smart cameras and development boards has slowly started to fade, since many development boards have inbuilt cameras and are tailored for computer vision applications.

With more and more applications being deployed on edge, it

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Components of Computer Vision

Hardware: Cameras/Imaging sensors

Software: Machine learning models for processing the images

Hardware: Processing units or conditional logic to automate application-specific use cases

COMPARISON OF SOME COMPUTER VISION BOARDS					
	Arduino Nicla Vision	OpenMV Cam H7	Huskylens	VC Picosmart	iVS-AWV3-AR0521
Processor	STM32H757AII6 Dual Arm Cortex M7/M4	ARM 32-bit Cortex-M7 CPU	Kendryte K21 (Al Chip with 64bit RISC-V dual core CPU)	FPGA module, High-end FPU processor	Allwinner V3 system on module
Camera/ image sensor	GC2145 (2MP colour camera)	OV7725 image sensor (can be interfaced with other sensors too)	HuskyLens – 0V2640 (2MP camera), Husky- Lens PRO – 0V5640 (5MP camera)	1-Megapixel CMOS sensor	ON Semiconductor AR0521 sensor module
Notable characteristics and features	MP34DT06JTR digital MEMS microphone, LSM6DSOXTR 6-axis IMU, NXP SE050C2 Crypto chip	Double-precision floating point unit (FPU), Hardware JPEG encoding	It can be used as a stand-alone product or as a peripheral for a microcontroller or microcomputer	VC realtime-OS for real- time image processing	H.264, 1080p60 video codec, integrated ISP supports up to 8MP with auto image function
Connecti-vity and communic-ation	Bluetooth, Wi-Fi, SPI, I2C, UART	Wi-Fi, BLE, SPI, I2C, UART, CAN	UART, I2C	FPC connector	Multiple wireless connectivity options available
Supported battery/ power source	Li-ion/LiPo single cell, 3.7V	3.7V LiPo battery	Two power pins on the 4-pin JST connector, micro USB connector	N/A	N/A
Suitable for	Asset tracking, object recognition, predictive maintenance	Object detection, line detection, video recording, QR code detection/deco-ding, eye tracking, colour tracking, frame differencing	object tracking, object recognition, line tracking,	Object recognition, position control, barcode reading, Web edge and level control	Autonomous vehicle and aftermarket home automation camera systems
Compatibi-lity/ integrations	Arduino IDE, Arduino CLI, Arduino IoT Cloud, OpenMV IDE	Edge impulse CLI, OpenMV IDE.	Mind+Editor, Arduino IDE	N/A	N/A
Primary programm- ing language	C/C++	Python	C/C++	N/A	N/A
Edge capability	Yes	Yes	Yes	Yes	Yes
Industry-ready?	Yes	Yes	More suited to prototyp- ing	Yes	Yes
Dimensions in mm (I x w x h)	22.86 x 22.86	45 x 36 x 30	52 x 44.5	22 x 23.5	N/A
Weight	2gm	19gm	N/A	N/A	N/A
MSRP	\$115.00	\$65.00	\$54.90	Quote based	Quote based
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makes more sense to deploy such devices on development boards. Here are some critical features that you need to consider while selecting a development board for your computer vision application:

- High processing speed
- Edge processing ability
- Faster access to memory
- Low power consumption
- Portable
- Inexpensive

While there are embedded platforms specifically for AI like NVIDIA Jetson and Raspberry Pi Compute, AI accelerators like Google TPU, SOMs/SBCs containing high-quality

imaging sensors, we will focus on a few boards that were designed for computer vision and compare some of their features.

Current trends in computer vision

Computer vision on edge. To successfully deploy computer vision on edge, neural network design must be integrated into embedded platforms. Then, there are also difficult design decisions to be made around power consumption, cost, accuracy, and flexibility. Overall, for bandwidth, speed, and security reasons, edge computing is critical

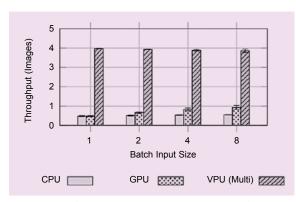
to computer vision processes. Still, hackers could take advantage of emerging security flaws in artificial intelligence. Designers must take this into consideration too.

However, there are some challenges when it comes to vision AI on edge, such as involving all edge endpoints can lead to complex device architectures. This makes device management and edge computing implementation harder. Edge computing differs from cloud architecture as it is spread out rather than centered around a single cloud. For large enterprises, it may be assumed that such an infrastructure

will have higher maintenance costs than one centered upon the cloud.

Embedded vision and IoT em**bedded vision**. Embedded vision is when we combine cameras with the processing unit, like smart cameras mentioned before. Many companies, like Lattice Semiconductor and Blaser AG, provide embedded vision solutions. For example, Basler's AI Vision Solution Kit comes with cloud connectivity and contains a dart MIPI camera module, an NVID-IA Jetson Nano development board, lens, and cable. It allows developers to test AI based IoT applications on an optimised vision system and to access cloud services.

But we are about to enter the next stage of the Internet of Things. So far, the focus was on connecting devices, accumulating data, and creating big data platforms. Now, our focus will move to making the 'things' smarter using technologies like computer vision and deep learning on edge. This way, the things will be able to make informed decisions about their surroundings. That's IoT embedded vision.



Throughput performance comparison per watt using the CPU, GPU, and multi-VPU configurations (Credit: https://arxiv.org)



Blaser's Al Vision Kit (Credit: Blaser AG)

Vision processing units (VPUs). A VPU is a specific type of AI accelerator designed for computer vision tasks. It is well suited for parallel processing. A very popular edge AI accelerator is the Intel Neural Compute Stick 2, which is built on the Myriad X VPU. It offers an easy-to-use USB interface.

Pre-trained convolutional network (CNN) models are used in the VPU chips to execute infer-

ence tasks. Studies have shown that VPUs are superior to CPUs and GPUs in terms of performance as well as power consumption. Moreover, a combination of multiple VPU chips can even reduce the thermal-design power (TDP) by up to eight times.

Software 2.0 and computer vision. As

we move towards Industry 4.0, software development is also moving towards Software 2.0. Software 1.0—the technology stack that includes languages like C/C++, python, etc—is the one that we are familiar with. It needs a coder. On the other hand, Software 2.0 literally writes itself when we feed enough data to a neural network that knows how to code.

This is yet another reason why the computer vision market will continue to grow exponentially. It will drive Software 2.0. Andrej Karpathy, Director of AI at Tesla, is a big proponent of Software 2.0. He notes that with Software 2.0, the computer vision library (for example, OpenCV) could even be autotuned on our specific data!

The author, Aaryaa Padhyegurjar, is an Industry 4.0 enthusiast at EFY with a keen interest in innovation and research.

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MULTI-USER CALL BELL For Up To Nine Persons

S.C. DWIVEDI

This unique but simple call bell can be used to call an office boy or a help at home from up to nine different places. Besides ringing, it shows the number of caller's room or place from where the call is being made.

The author's prototype is shown in Fig. 1 while its block diagram is shown in Fig. 2. The call bell is built around LM7805 voltage regulator IC1, ICs 74LS147, 74LS04, and 74LS247 (IC2-IC4), NE555 timer IC5, BC547 transistors T1 and T2, and 1N4148 signal diodes D1-D4.

IC1 provides 5V regulated power supply to the circuit except piezo buzzer PZ1, which is connected to the 9V battery directly.

IC2 is 10-line to 4-line priority encoder. It encodes nine data lines to four-line BCD. Zero condition

PARTS LIST

Semiconductors:	
IC1	- LM7805, 5V voltage
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IC2	- 74LS147 10-line to 4-line
	priority encoder
IC3	- 74LS04 hex inverter
IC4	- 74LS247 7-segment decoder
	driver
IC5	- NE555 timer
T1, T2	- BC547 NPN transistor
D1-D4	- 1N4148 signal diode
LED1-LED9	- 5mm LED
DIS1	- 7-segment display
	LTS-6960HR/LTS542
Resistors (all 1/4	-watt, ±5% carbon):
R1-R9, R11, R14	- 1-kilo-ohm
R10, R12, R13,	
R16	- 10-kilo-ohm
R15	- 2.2-kilo-ohm

C1 - 220μF, 35V electrolytic C2 - 0.01μF ceramic disk - 0.1µF ceramic disk

Miscellaneous:

Capacitors:

S1-S10 - On/off switch PZ1 - Piezo buzzer BATT.1 - 9V battery/regulated power supply

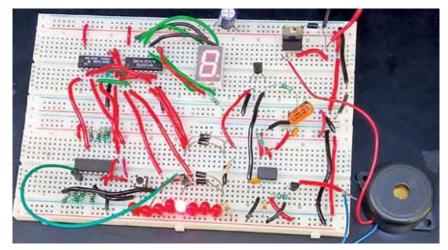


Fig. 1: Author's prototype

requires no input, as zero is encoded when all nine data lines are at a high logic level.

IC3 is hex inverter. which inverts output of 74LS147. Only gates IC3(A) through IC3(D) are used to invert the four-line BCD. The inverted outputs of IC3 are given to IC4 and the four diodes.

IC4 is a BCD-to-sevensegment decoder/driver. It has active-low outputs to directly drive the signal diodes. Output of IC4 is given to the common anode display LTS-6960HR/ LTS542 (DIS1), which is used to display the caller's room number.

The input switches (S1 through S9) are used to trigger timer IC5 through four signal diodes (D1 through D4). IC5 drives the piezo buzzer for a pre-set time.

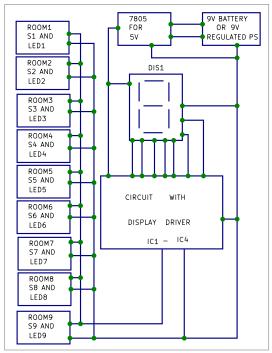


Fig. 2: Block diagram

Output of IC5 is decided by resistor R13 and capacitor C1.

The nine switches (S1 through

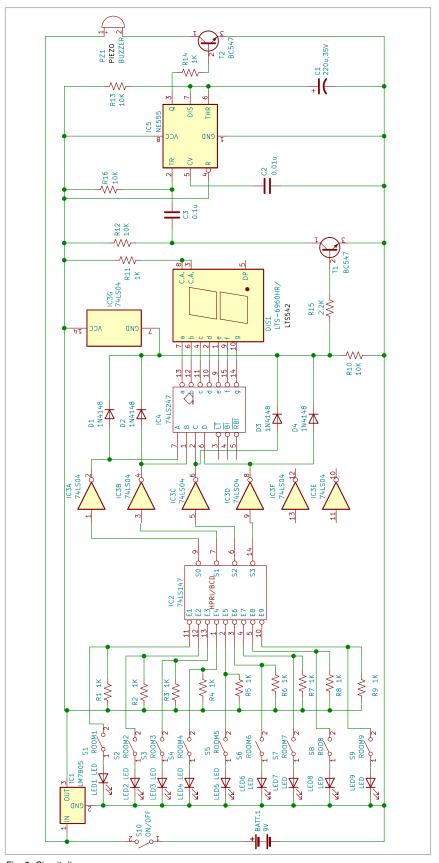
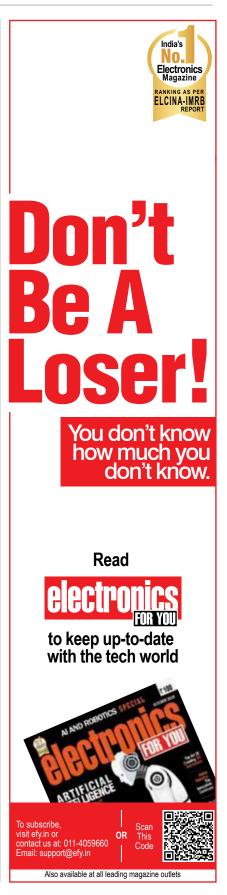


Fig. 3: Circuit diagram



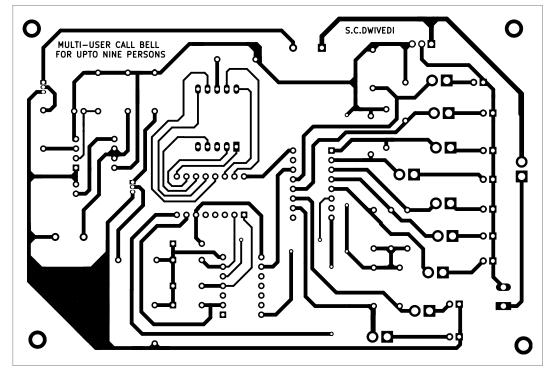


Fig. 4: Actual-size PCB

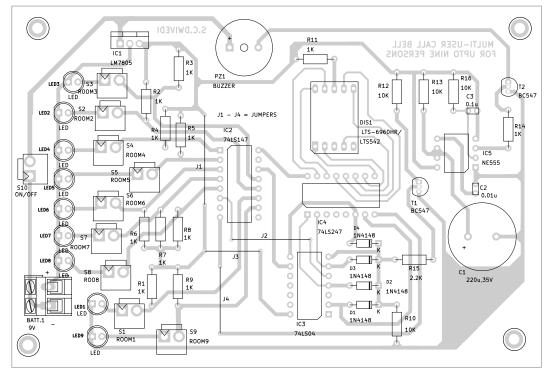


Fig. 5: Component layout of the PCB

S9) are installed at nine different places or rooms along with their corresponding LEDs (LED1 through LED9) for caller indications. IC2 encodes nine data lines into four BCD outputs.

When switches S1 through S9 are open and no LED is glowing,

the seven-segment display DIS1 shows 0. If any of the switches is closed, the BCD output goes low and is inverted by IC3(A) through IC3(D). Then that room number LED glows, and at the same time the seven-segment display DIS1 shows that room number.

Working of the circuit is simple. When, say, room number 1's switch S1 is turned on, its LED1 glows and DIS1 displays 1 besides sounding an alarm through piezo buzzer PZ1. The caller should switch off S1 after the call has been attended so that the system is ready to receive the next call.

An actual-size, single-side PCB layout for the call bell is shown in Fig. 3 and its component layout in Fig. 4. After assembling the circuit on PCB, enclose it in a suitable box. Install switches S1 through S9 along with their respective LEDs (LED1 through LED9) in nine different rooms or locations. The assembled PCB, including DIS1 and speaker, should be placed where the

attendant is stationed. EFY

S.C. Dwivedi is an electronics enthusiast and circuit designer at EFY





First Ever Finger-Size **FULL-TOUCH E-INK PHONE**

ASHWINI KUMAR SINHA

hones are getting smarter and complex with many functions that most of us find difficult to use. The modern smartphone uses either backlight display or an

OLED display that can harm our eyes on prolonged use.

You may have wondered how phones are designed and whether you could make your own smart-



Fig. 1: Author's prototype

phone. So, here is how you can make your own smartphone having an impressive E-ink technology. This full-touch, finger sized phone uses UI-powered Linux operating system (OS) that can be accessed using any OS and VNC HDMI. It can perform all the basic functions of a phone.

The author's prototype is shown in Fig. 1 and the prototype with its calling screen is shown in Fig. 2. It is so small (see Fig. 3) that it can be strapped to your little finger.

E-ink technology saves power due to absence of backlit display and is good for those who want to avoid the glare of OLED and backlit displays. E-Ink display, like the printed paper, remains visible even when power is cut off, until you refresh it.

However, this is not all. We are going to develop the E-Ink phone,



Fig. 2: Author's prototype with calling screen



Fig. 3: E-ink phone size compared to a finger

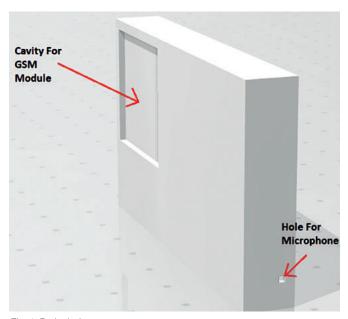


Fig. 4: Body design

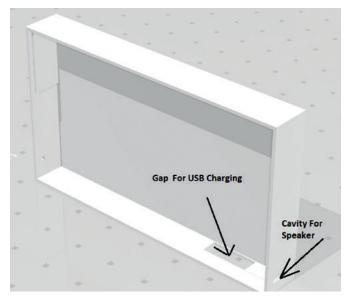


Fig. 5: Holes made in the phone's body

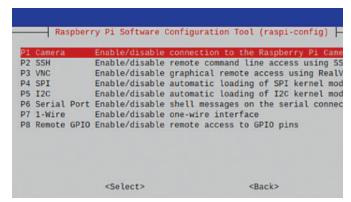


Fig. 6: Software configuration

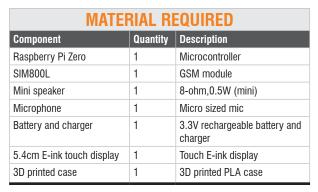




Fig. 7: UI for dialing screen

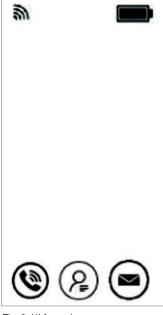


Fig. 8: UI for main screen

and, without any extra cost, you will get a full Linux based portable finger-sized computer as well! So, let us start collecting the required components, which are listed in the table above.

Designing

Though it is a totally home-made DIY phone, we can design it to look slim and attractive. So, for the phone, make its body the same size as the E-ink touch display. Then make space, as shown in Fig. 4, for embedding the GSM module inside the phone body.

Next make a tiny hole where the speaker is to be placed, so that you can hear the sound (see Fig. 5). Similarly, make holes for the mic and power input. Now, after designing the phone body, 3D print it.

Prepare the Raspberry Pi with its latest OS and then enable the SPI and I2C and serial ports of the Raspberry Pi. To do that, run the following command in Llinux terminal and enable software configuration one by one, as shown in Fig. 6.

```
sudo raspi-config
```

Next, install driver for the E-ink touch display and the Python modules to use the display. To do that, open the Linux terminal and run the following commands:

```
wget http://www.airspayce.com/mikem/
bcm2835/bcm2835-1.68.tar.gz
tar zxvf bcm2835-1.68.tar.gz
cd bcm2835-1.68/
sudo ./configure && sudo make && sudo make
check && sudo make install
sudo apt-get install wiringpi
#For Pi 4, you need to update it:
wget https://project-downloads.drogon.
net/wiringpi-latest.deb
sudo dpkg -i wiringpi-latest.deb
gpio -v
#You will get 2.52 information if you
install it correctly
sudo apt-get update
sudo apt-get install python3-pip
sudo apt-get install python3-pil
sudo apt-get install python3-numpy
sudo pip3 install RPi.GPIO
sudo pip3 install spidev
cd ~
git clone https://github.com/waveshare/
Touch e-Paper HAT
```

Creatina UI

We need to create the user-interface (UI) for the phone so that it looks beautiful. You can choose any icon and design a custom UI of your choice. For the prototype, round icons were used, as shown in Fig. 7. For UI, we must have the basic design to perform the basic functions. So, here is the list of UI pages for the basic phone functions:

- Main screen
- Dialing screen
- · Contacts screen
- Messages
- Incoming call
- Calling screen

For every screen UI we need to

```
File Edit Format Run Options Window Help
#!/usr/bin/python
                                                                                                         amples
  -*- coding:utf-8 -*-
import sys
import os
from gsmHat import GSMHat, SMS, GPS
gsm = GSMHat('/dev/ttyS0', 4800)
                                                                                                          dfe.py
picdir = os.path.join(os.path.dirname(os.path.dirname(os.path.realpath(__file__)
libdir = os.path.join(os.path.dirname(os.path.dirname(os.path.realpath(__file__))
   os.path.exists(libdir):
     sys.path.append(libdir)
from TP_lib import gt1151
from TP_lib import epd2in13_V2
import time
import logging
import traceback
from PIL import Image, ImageDraw, ImageFont
import threading
logging.basicConfig(level=logging.DEBUG)
flag_t = 1
```

Fig. 9: Python code

```
logging.info("epd2in13_V2 Touch Demo")
font15 = ImageFont.truetype(os.path.join(picdir, 'Font.ttc'), 15)
font24 = ImageFont.truetype(os.path.join(picdir, 'Font.ttc'), 24)
                                                                                                                                                dfe.py
                                                                                                                                                                phoneUI 1
epd = epd2in13_V2.EPD_2IN13_V2()
gt = gt1151.GT1151()
GT_Dev = gt1151.GT_Development()
GT_Old = gt1151.GT_Development()
logging.info("init and Clear")
epd.init(epd.FULL_UPDATE)
gt.GT_Init()
 epd.Clear(0xFF)
image = Image.new('1', (epd.height, epd.width), 255) # 255: clear the frame draw = ImageDraw.Draw(image) draw.rectangle((0, 10, 200, 34), fill = 0)
t = threading.Thread(target = pthread_irq)
t.setDaemon(True)
t.start()
draw.rectangle([(0,0),(50,50)],outline = 0)
draw.text((1, 1), 'Ahekllo cgghhg', font = font15, fill = 0)
epd.display(epd.getbuffer(image))
#i = j = k = ReFlag = SelfFlag = Page = Photo_L = Photo_S = 0
previous=0
image = Image.open(os.path.join(picdir, 'phone screen1.png'))
epd.display(epd.getbuffer(image))
```

Fig. 10: Home screen

```
while(1):
     gt.GT_Scan(GT_Dev, GT_Old)

if(GT_Old.X[0] == GT_Dev.X[0] and GT_Old.Y[0] == GT_Dev.Y[0] and GT_Old.S[0] == GT_Dev.S[0]):
     if (Page==0):
          inage = Image.open(os.path.join(picdir, 'phone screeni.png'))
epd.display(epd.getbuffer(image))
if(GT_Dev.X[0] > 0 and GT_Dev.X[0] <15 and GT_Dev.Y[0] > 200 and GT_Dev.Y[0] < 248):
    print("h")</pre>
               Page=1
          if(GT_Dev.X[0] > 30 and GT_Dev.X[0] <60 and GT_Dev.Y[0] > 200 and GT_Dev.Y[0] < 248):
               print("contact")
Page=2
          if(GT_Dev.X[0] > 80 and GT_Dev.X[0] <130 and GT_Dev.Y[0] > 200 and GT_Dev.Y[0] < 248):
    print("h")
    Page=3</pre>
    if(Page == 1):
          print (number)
          if(previous!=Page):
    image = Image.open(os.path.join(picdir, 'dialing screen.png'))
                epd.display(epd.getbuffer(image))
                previous=Page
         if(GT_Dev.X[0] > 0 and GT_Dev.X[0] < 20 and GT_Dev.Y[0] > 79 and GT_Dev.Y[0] < 88):
    print("1")
    number+= "1"
    print (number)</pre>
                draw.text((20, 20), "vhgvjhj", font = font15, fill = 0)
               epd.display(epd.getbuffer(image))
```

Fig. 11: Call function

get the icon, resize, and place it such that it fits inside the actual size of the phone's display (5.4cm size in prototype). For the main screen UI, battery icon is used for getting power information, tour icon for getting the signal info, and dial icon for dialing screen, contacts, and messaging

screens. In the middle, it is kept blank to display time, date, etc.

Create the dialing screen UI for the numbers and other icons. Dial a number and go to home screen, or erase and refresh the screen. Similarly, make UIs for the remaining phone functions and screen.

```
elif(GT_Dev.X[0] > 40 and GT_Dev.X[0] < 60 and GT_Dev.Y[0] > 190 and GT_Dev.Y[0] < 220):
       print("call")
        gsm.Call(number)
                                # This call hangs up automatically after 15 seconds
       Page = 4
   if(GT_Dev.X[0] > 30 and GT_Dev.X[0] < 60 and GT_Dev.Y[0] > 225 and GT_Dev.Y[0] < 250):
        image = Image.open(os.path.join(picdir, 'phone screen1.png'))
        epd.display(epd.getbuffer(image))
       Page=0
if(Page == 2):
   print("cintact")
   image = Image.open(os.path.join(picdir, 'contactsscreen.png'))
   epd.display(epd.getbuffer(image))
    if(GT_Dev.X[0] > 30 and GT_Dev.X[0] < 60 and GT_Dev.Y[0] > 225 and GT_Dev.Y[0] < 250):
       print("home")
       Page=0
if(Page == 3):
   print("Message")
   image = Image.open(os.path.join(picdir, 'contactsscreen.png'))
   epd.display(epd.getbuffer(image))
   print("message c=screen")
   newSMS = gsm.SMS_read()
```

Fig. 12: Message function

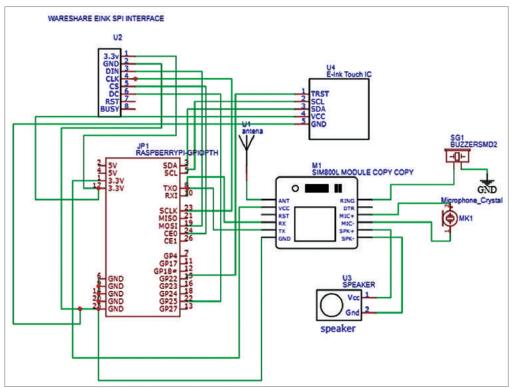


Fig. 13: Circuit diagram

Save the images from the UIs in the pic folder of the Python library that was cloned earlier. Open the code for displaying.

Coding phone OS

To create the code for OS for the phone, import the modules and library for interfacing the E-ink touch display. Then import the Python modules for using SIM 800L GSM module. Next, set the path where you keep the UI images for the phone and create the while loop to check the screen. The page to display is similar to previous, but new if its previous one has new page number. Update the display with UI pic.

For the (first) home page, check the touch points for the icons like dial icon, contact icon, or message icon. Also check the incoming call by checking the ring pin. Then create an 'if' condition where you check the touch points to match the range of icons on the UI. If you touch the phone icon, the touch point should

> match with the touch point corresponding to the phone.

Set the function in if condition to change the page number to next (No. 2). That is, in the UI image of dialing screen, update the phone screen and set the page number to 2. Now the phone moves to dialing screen with buttons of numbers. Create the if conditions for checking the numbers and icons.

Touch the dial pad screen if touch point range of number is one or two, or other number button. Update an empty string named number, add that number to the string. If touch point matches with the call icon, it sends the com-

DO-IT-YOURSELF: PROJECT



Fig. 14: E-ink header-to-header pins on Raspberry Pi

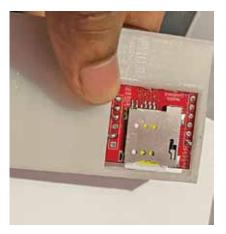


Fig. 15: GSM module fixed in the enclosure



Fig. 16: Phone screen fixed on the base phone cover

mand to GSM module to call that number. It also sets the page number to the calling screen and updates the display with calling screen.

In the calling screen, check the touch points for Call Hang icon. If your touch point matches with the range of call-hang icon, touch point will send the command to SIM800L GSM module to cut the call and hang up. It will also change the page number to 0 and update the display with home screen UI.

Similarly, check the touch points and incoming call and then change the page number and update the display screen with UI for the message, contact, or ringing screen. Then in that screen, check the touch point for the button icon, touch point range, and set the function for those buttons—like hanging phone, home, main screen, calling, and other functions for the button icons. The Python code for achieving the above-mentioned actions are shown in Fig. 9 through Fig. 12.

Connection

After coding, connect the components as per the diagram shown in Fig. 13. Place the Raspberry Pi and SIM 800L module inside in the enclosure/body that has been designed. Fix the speaker and microphone against the holes made in the enclosure for them.

Fix the battery and charging unit in between the display and Raspberry Pi. Then mount the display on the enclosure with the pins matching the E-ink female header-to-header on Raspberry Pi, as shown in Fig. 14. Fix the GSM module on the slot, as shown in Fig. 15, and the phone

The source code of this project for free download at



Fig. 17: E-ink phone after assembly

screen on the base phone cover, as shown in Fig. 16.

Testing

The E-ink phone is now ready to show its magic. Insert the SIM in GSM module and run the OS code. and you are ready to use it.

Tap on on the Call icon and you get the dialing screen. Key in a phone number and touch on Call icon to make the call. Touch the Home icon to get the home screen and touch Contacts to see the contact numbers.

Next, try making a call from some other phone to your E-ink phone. When connected, the buzzer starts ringing and it takes you to the ringing screen. Here, touch the Call Attend icon to attend the call and Call Hang icon to hang up the call.

The final assembled E-ink phone is shown in Fig. 17. Enjoy! EFY

Note. This is the first version of the phone. The author keeps adding new features and improvements on www.electronicsforu.com

> Ashwini Kumar Sinha is a technology enthusiast at EFY

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